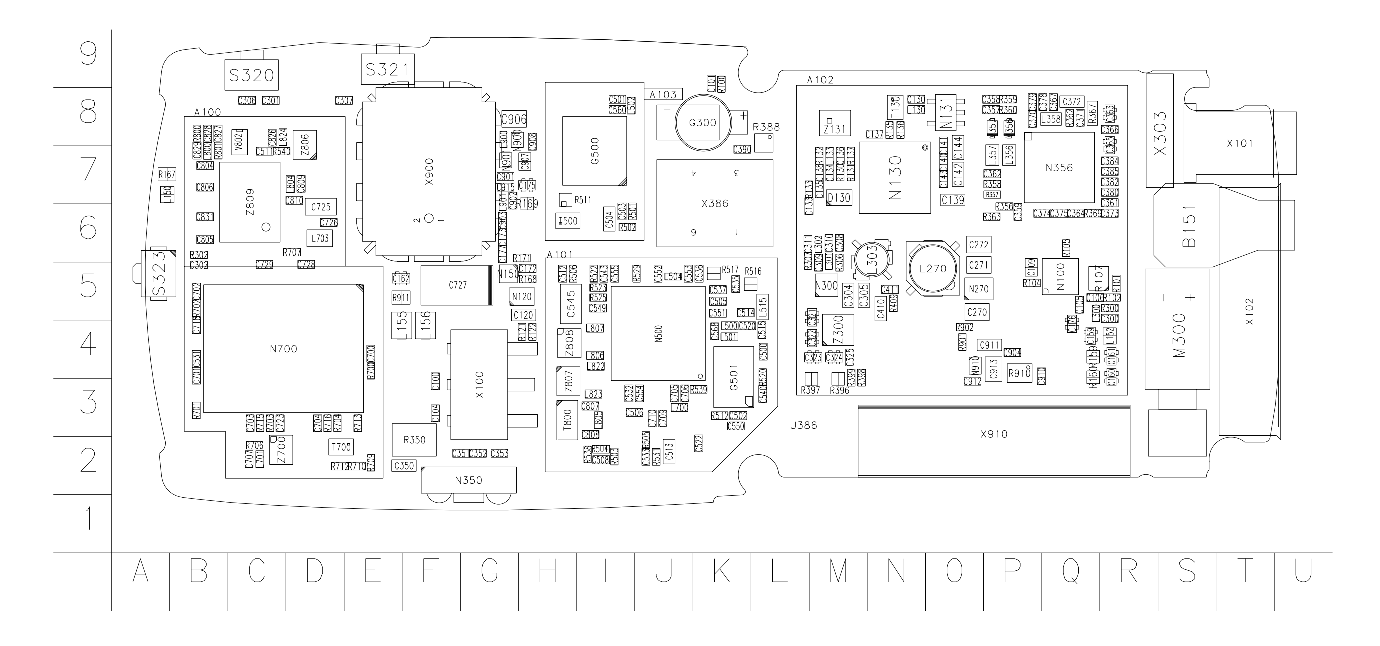
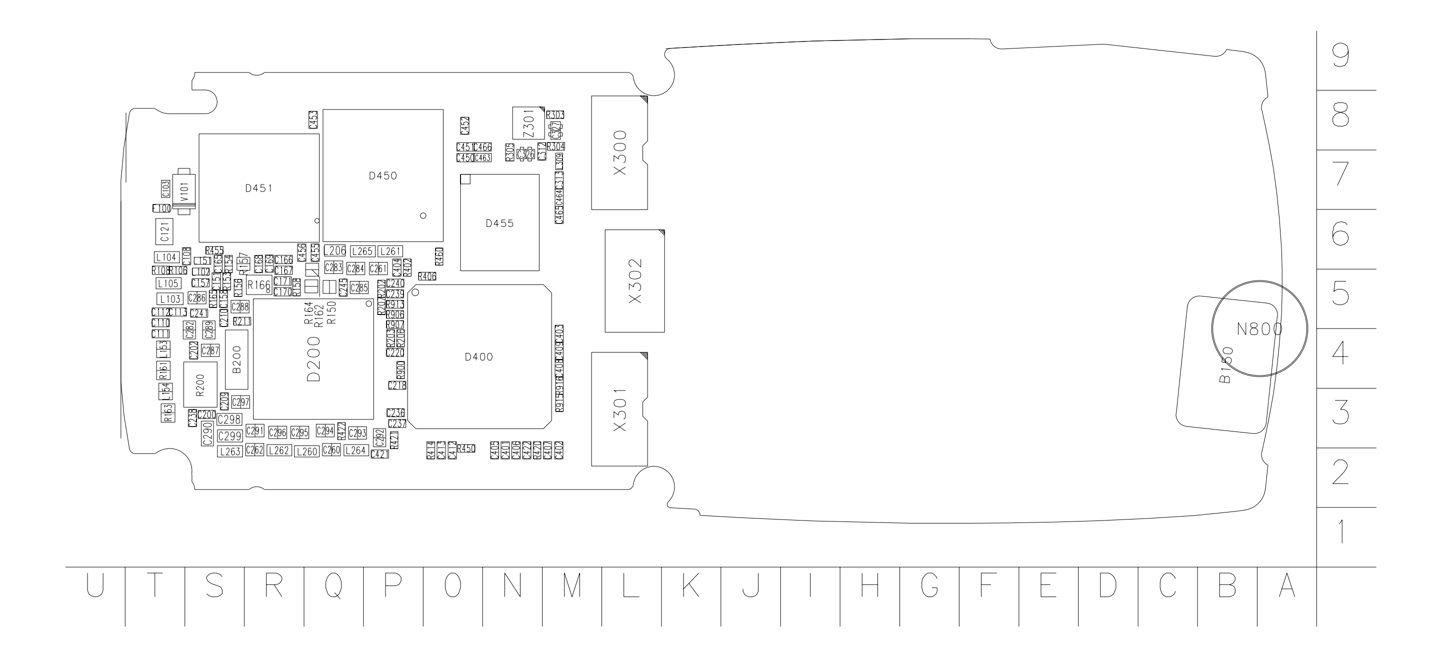
# Component layouts

*The component layouts are shown in A3 format in the schematics section.*

## Component layout (top side), 1maa_02



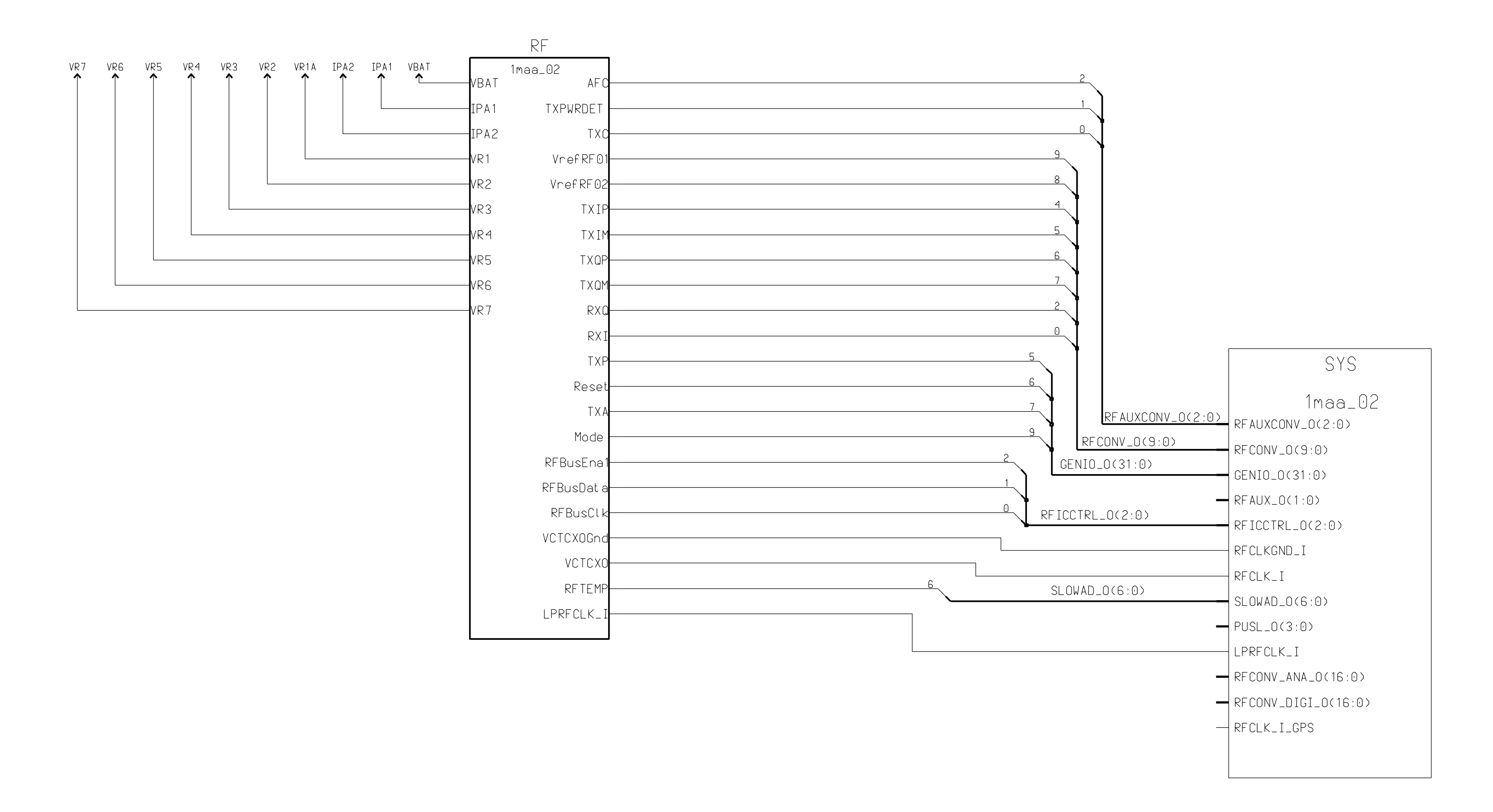

## Component layout (bottom side), 1maa_02





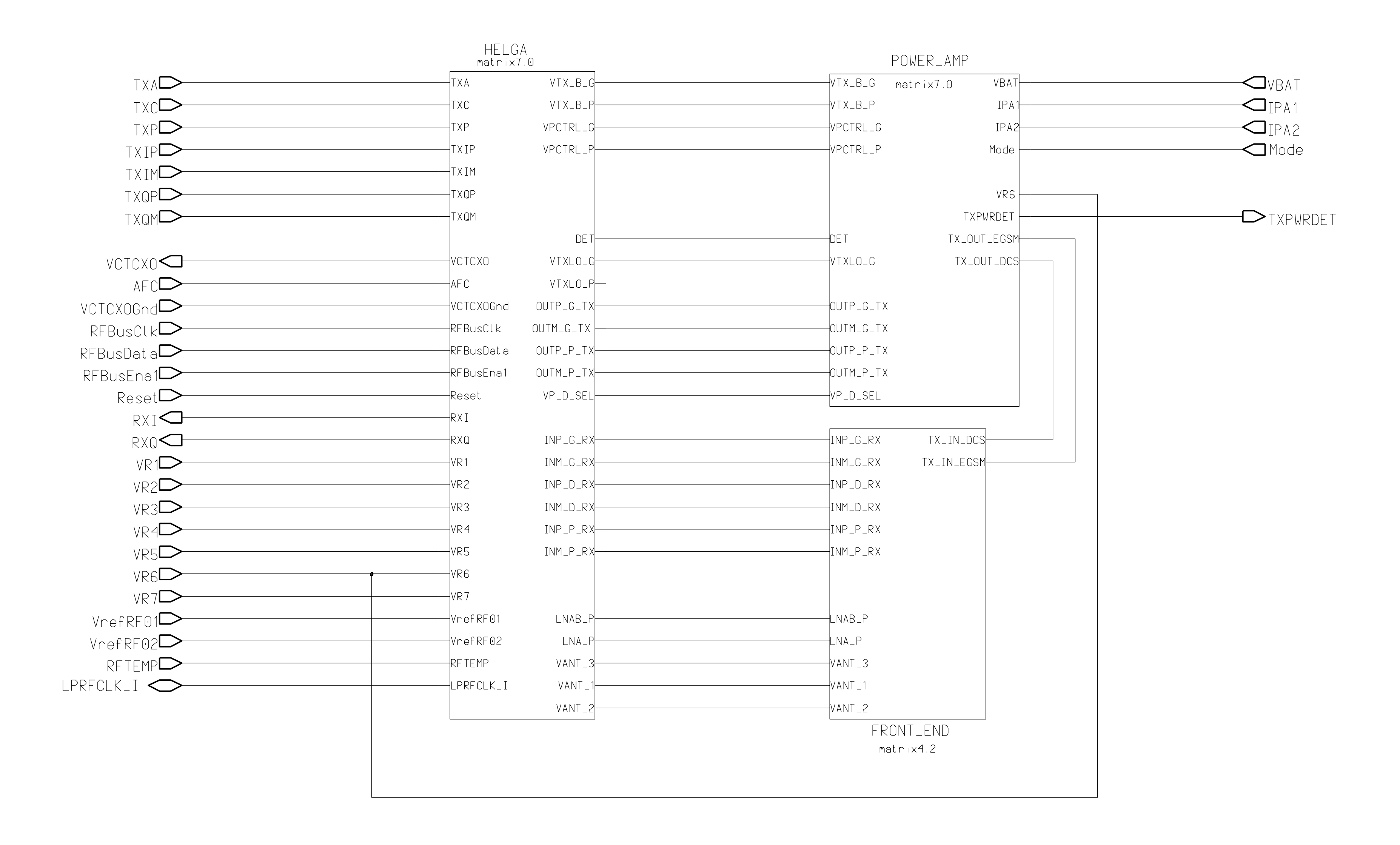
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**Top Level, 1maa_02, v.0 ed. 40**



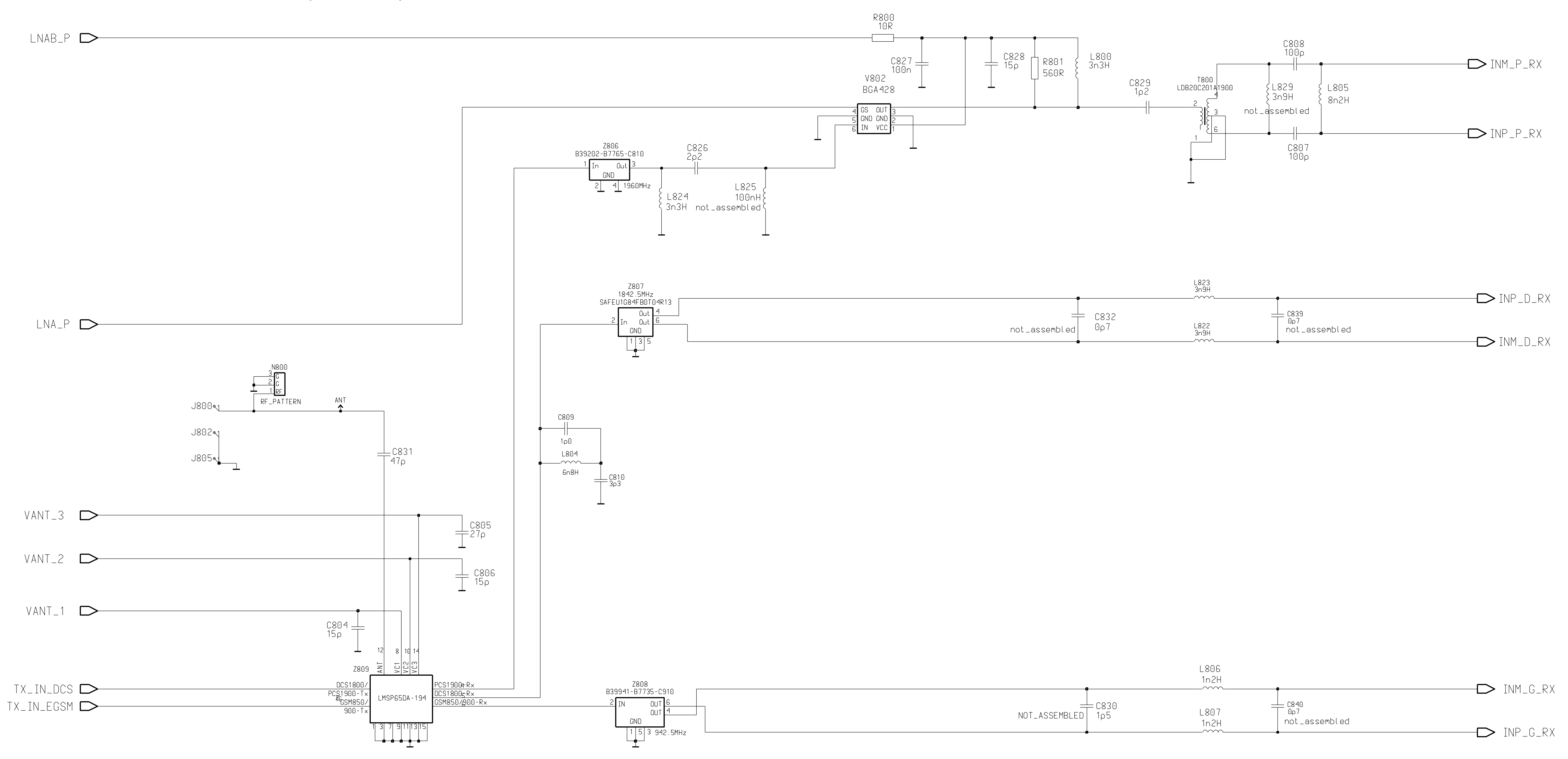

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**RF Top Level,1maa_02, v. 0.0 ed. 67**



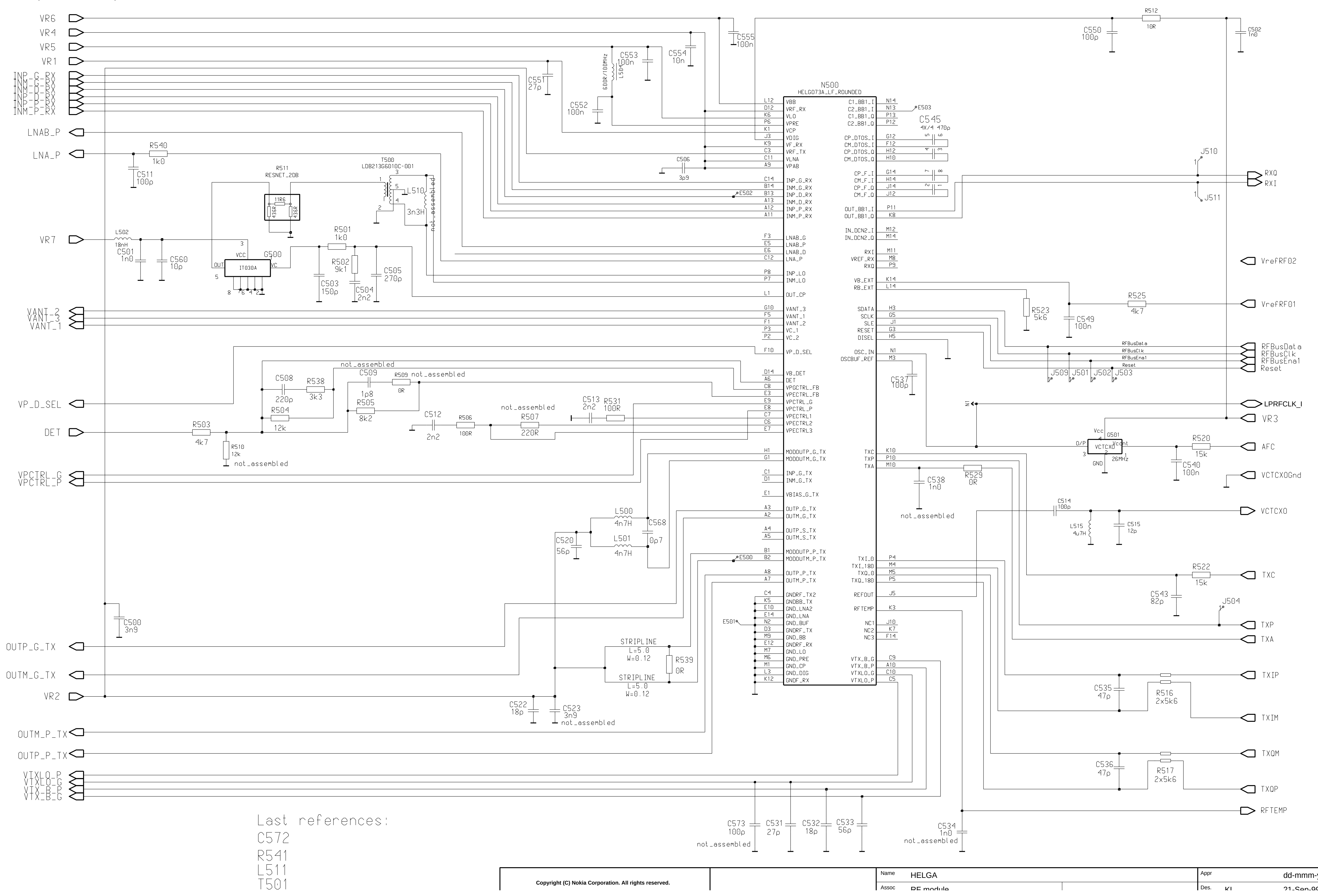

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**RX Front End and Antenna Switch, 1maa_02, v. 0.0 ed. 79**



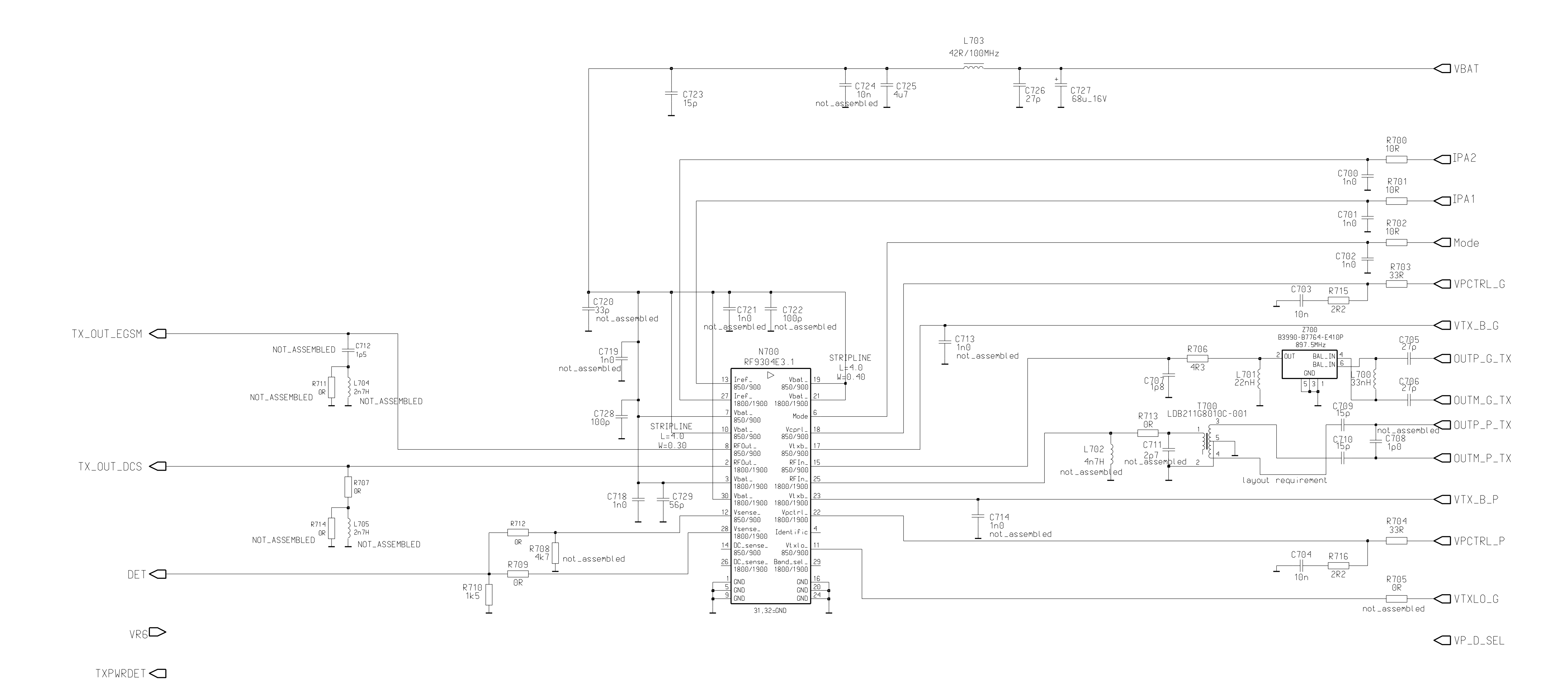

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**HELGA, 1maa_02, ed. 98**




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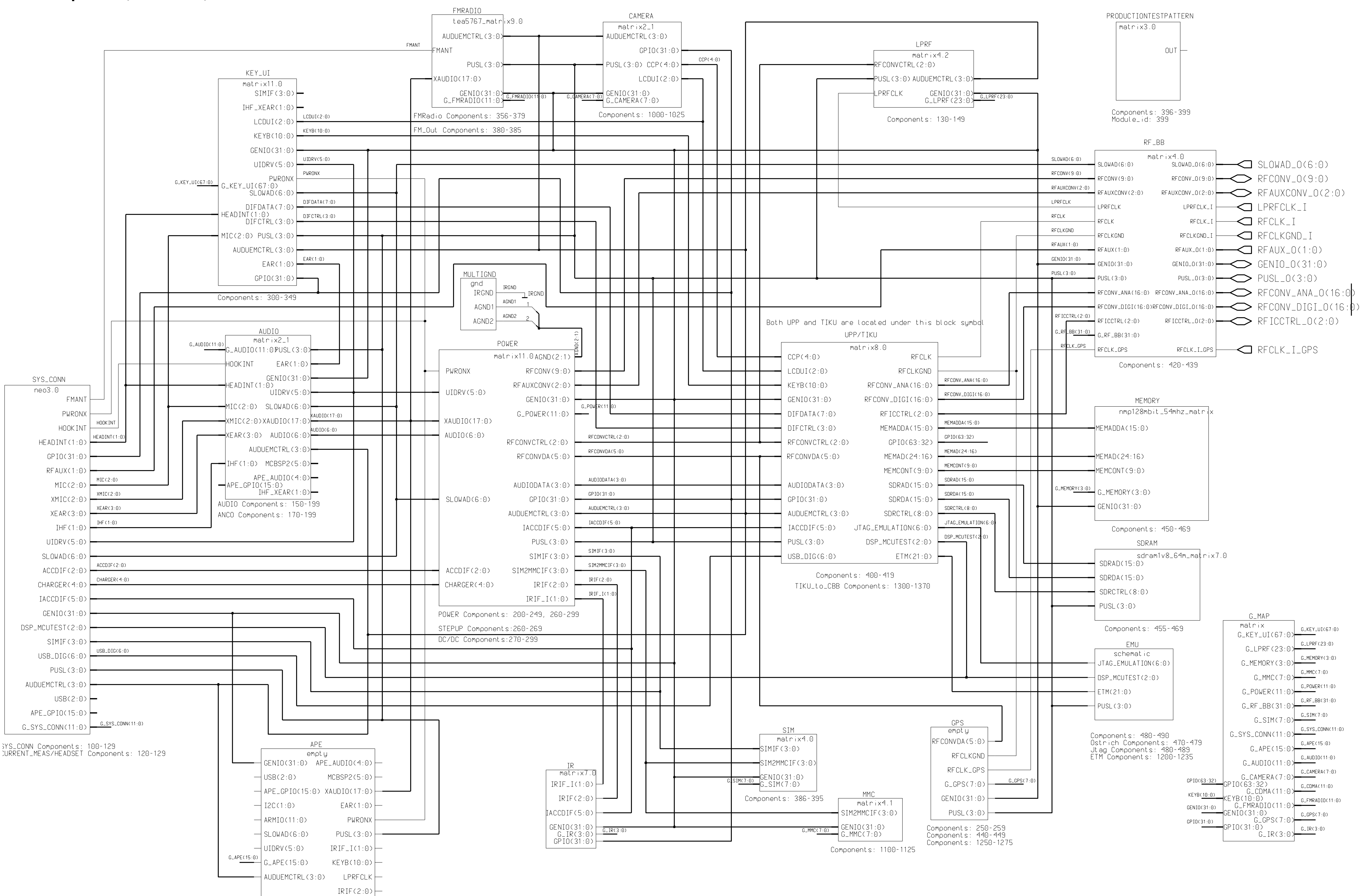
**RF9204 Power Amplifier and Power Detection, 1maa_02, v. 0.2 ed. 65**




Last references:
C731
R714
L705

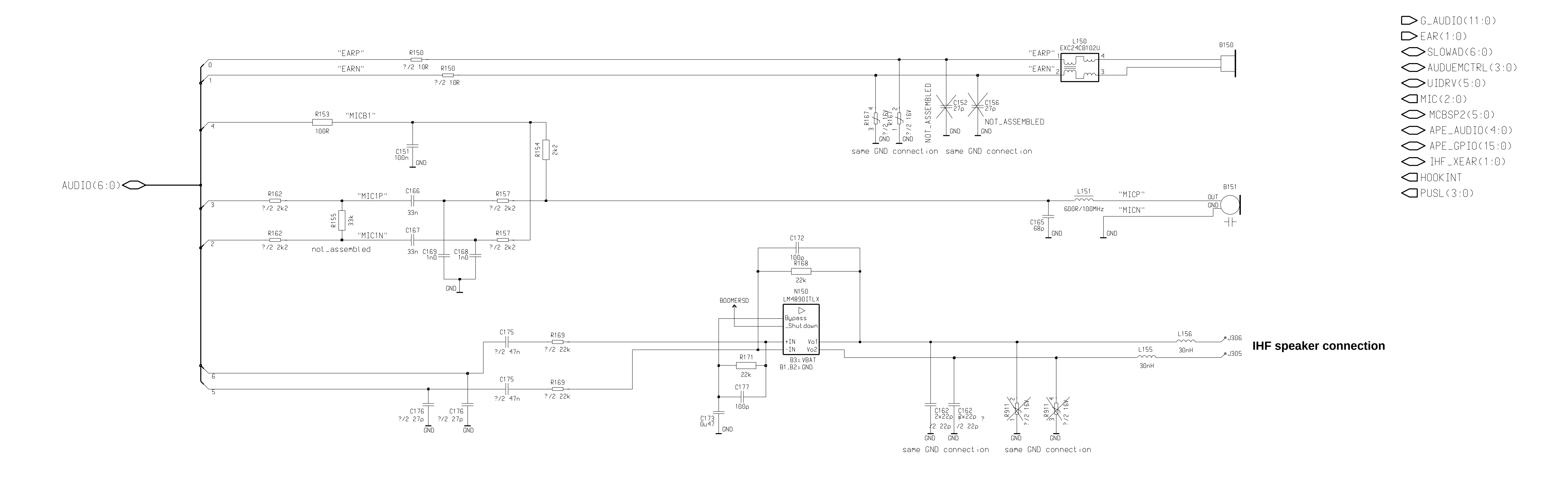

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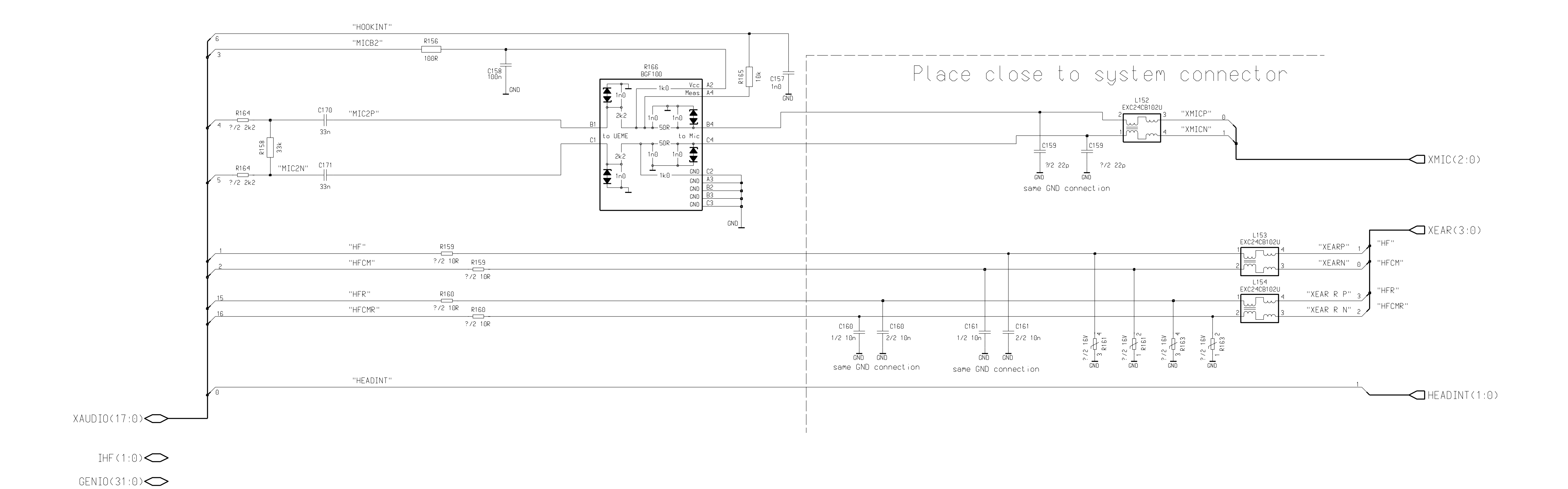
## BR4.5 Top Level, 1maa_02, v. 6.1 ed. 43




## BR45 Audio, 1maa_02, v. 1.6 ed. 190

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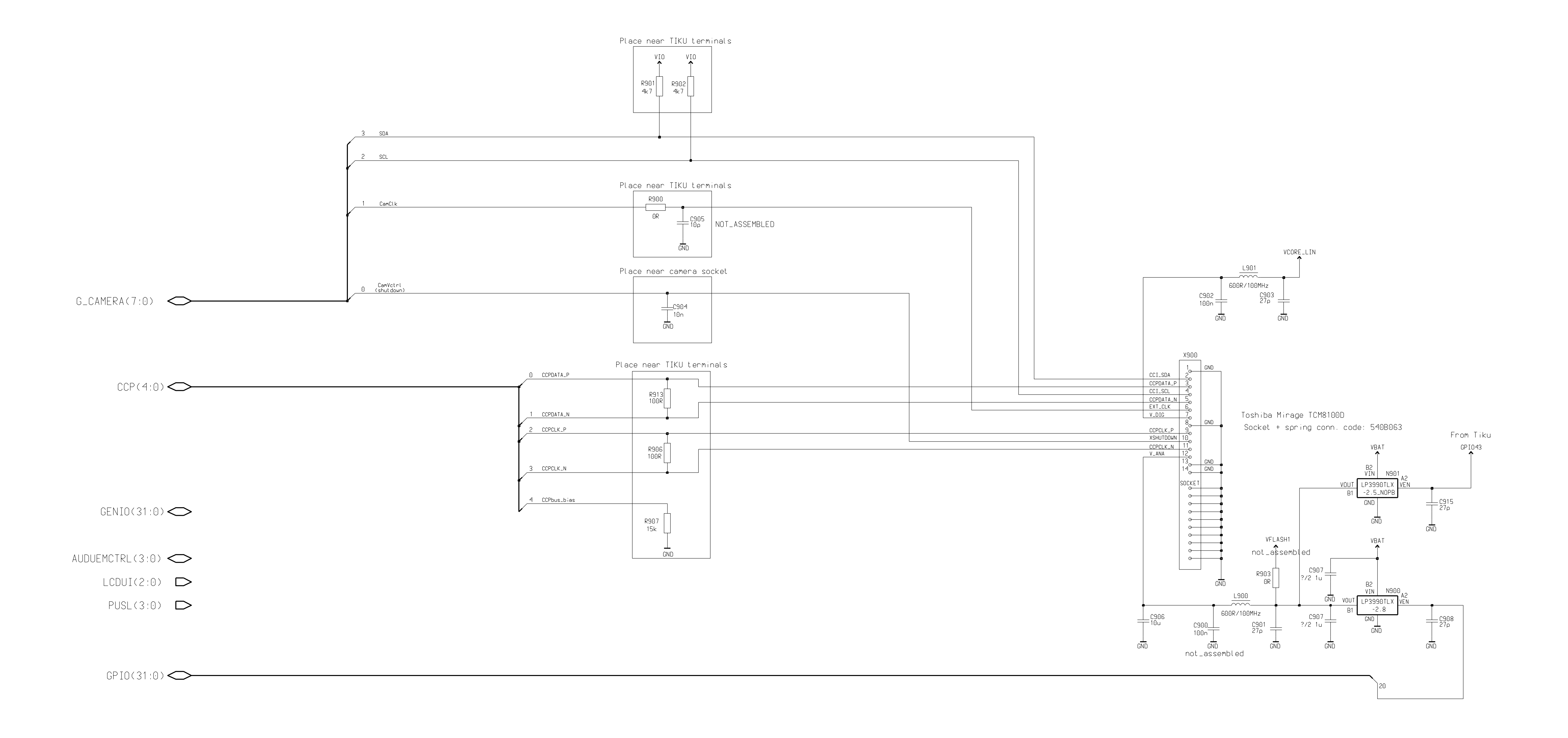




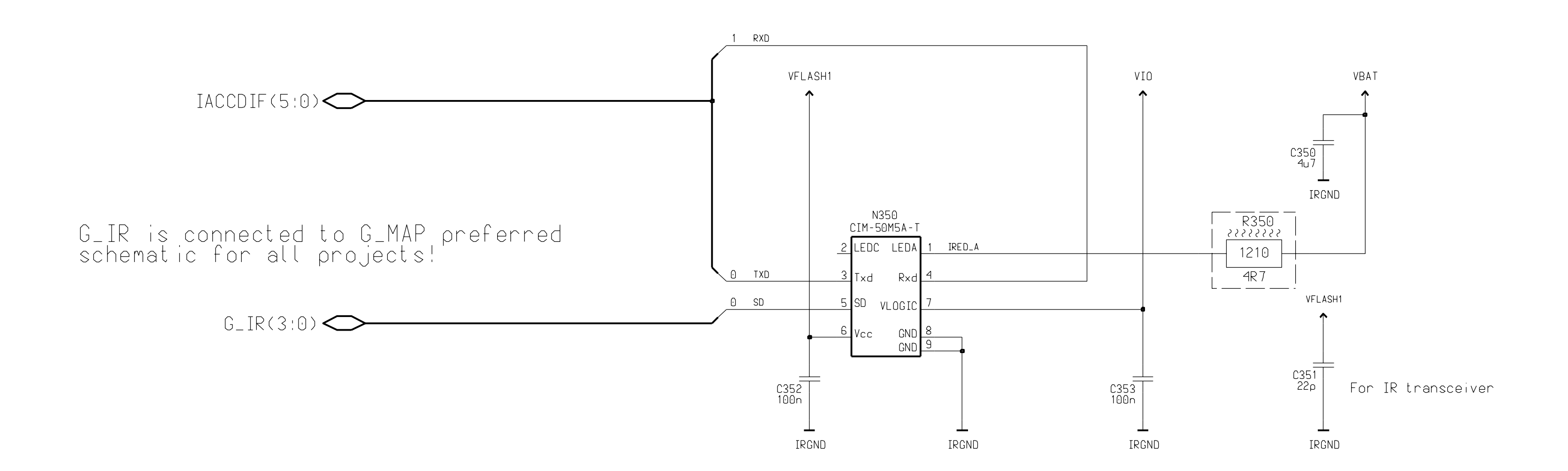
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**Mirage 2 VGA Camera, 1maa_02, v. 1.1 ed. 206**

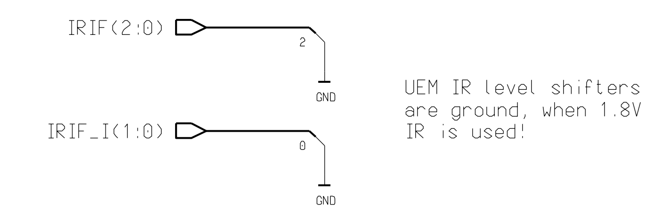



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**Citizen IR Module 1.8V, 1maa_02, v. 6.1.8 ed. 56**




GENIO(31:0)
GPIO(31:0)

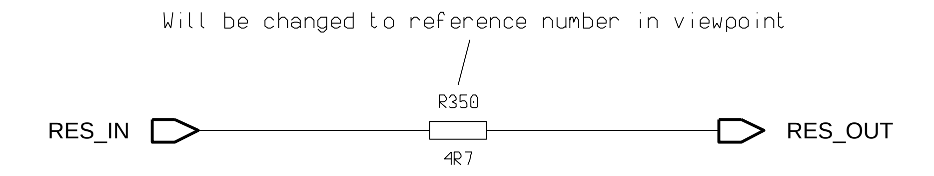



Used referenses

C 350 - 353
N 350
R 350

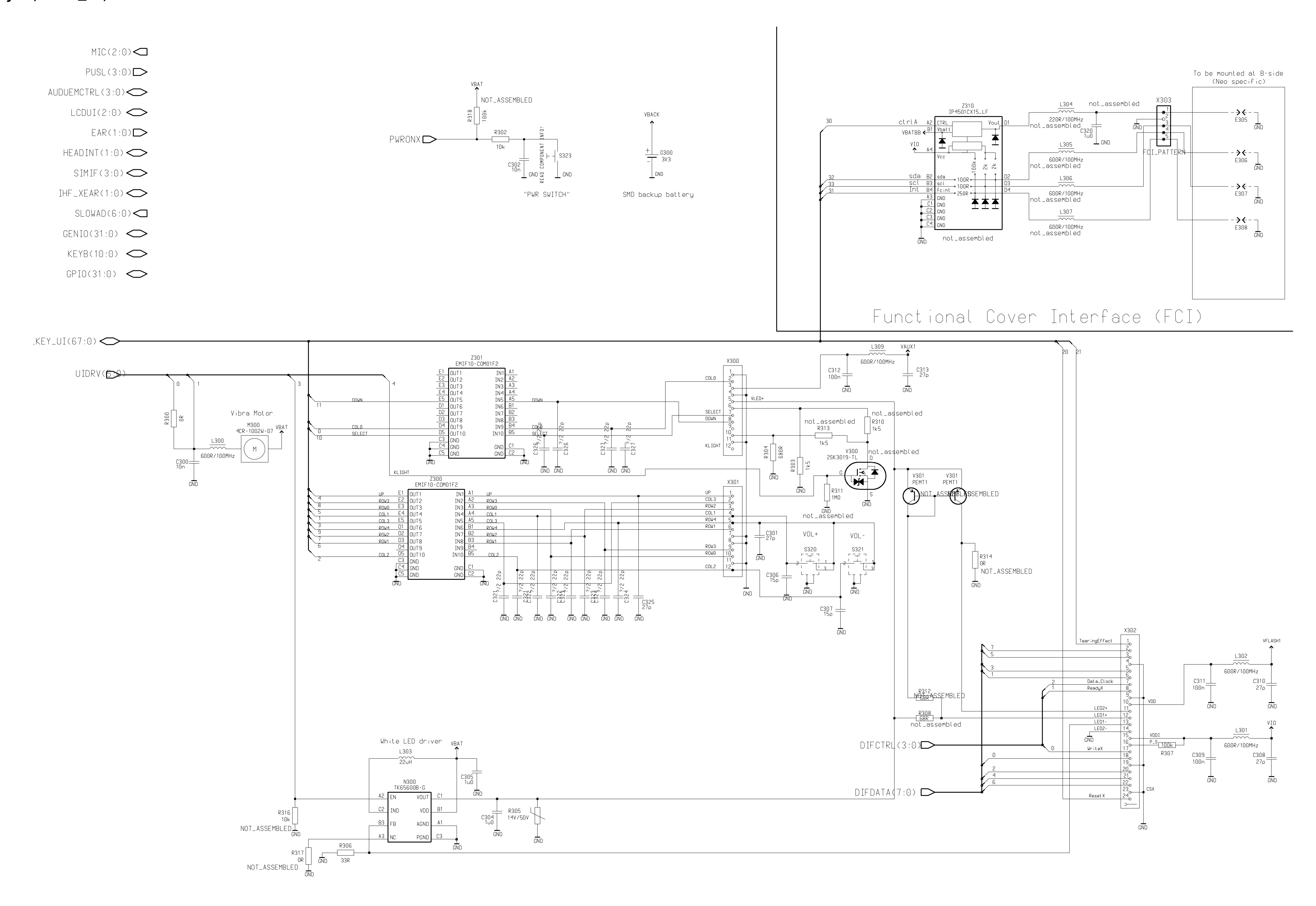

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**IR Resistor 1210, 1maa_02, v. 0 ed. 6**



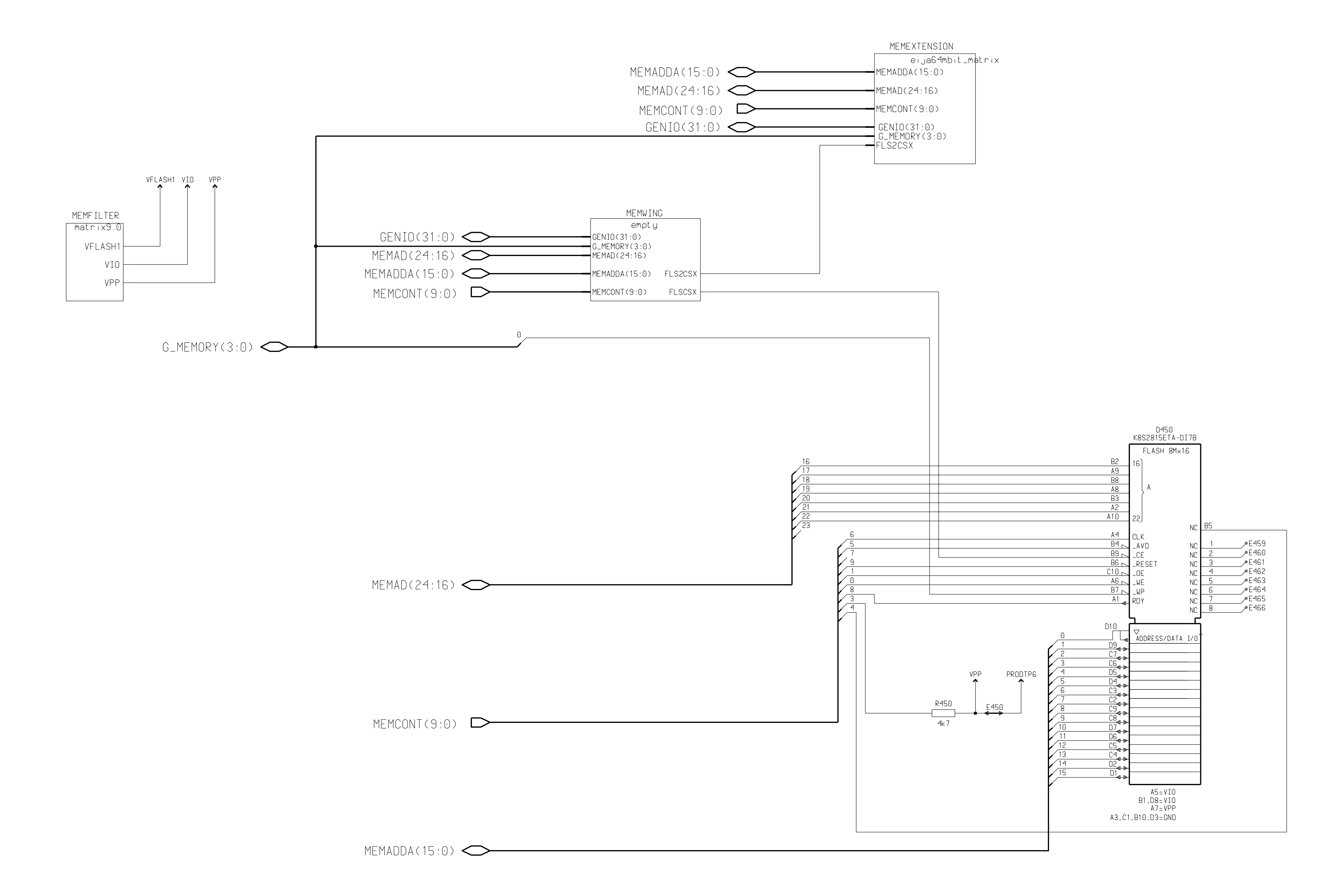

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**Key UI, 1maa_02, v. 1 ed. 350**




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**Flash Memory 128 Mbit, 1maa_02, v. 2.0 ed. 81**

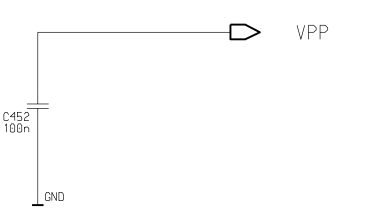



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## Discrete Capacitors for two Memories without VFlash1, 1maa_02, v. 1.3 ed. 14

Decoupling capacitors for 1st flash

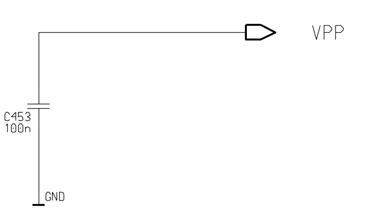



Decoupling capacitors for 2nd flash



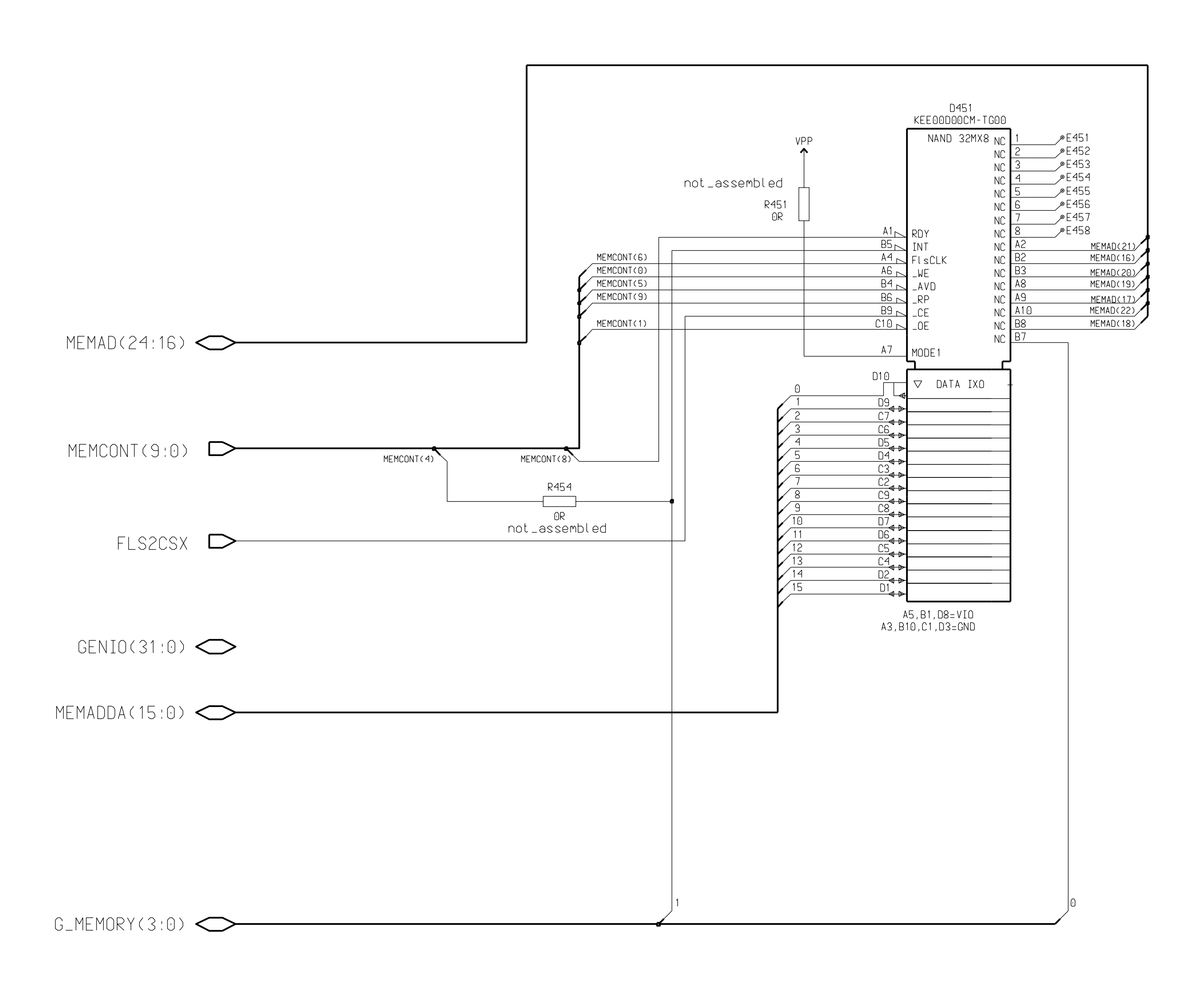

VFLASH1





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**Flash Memory 64 Mbit NAND, 1maa_02, v. 2.0 ed. 68**

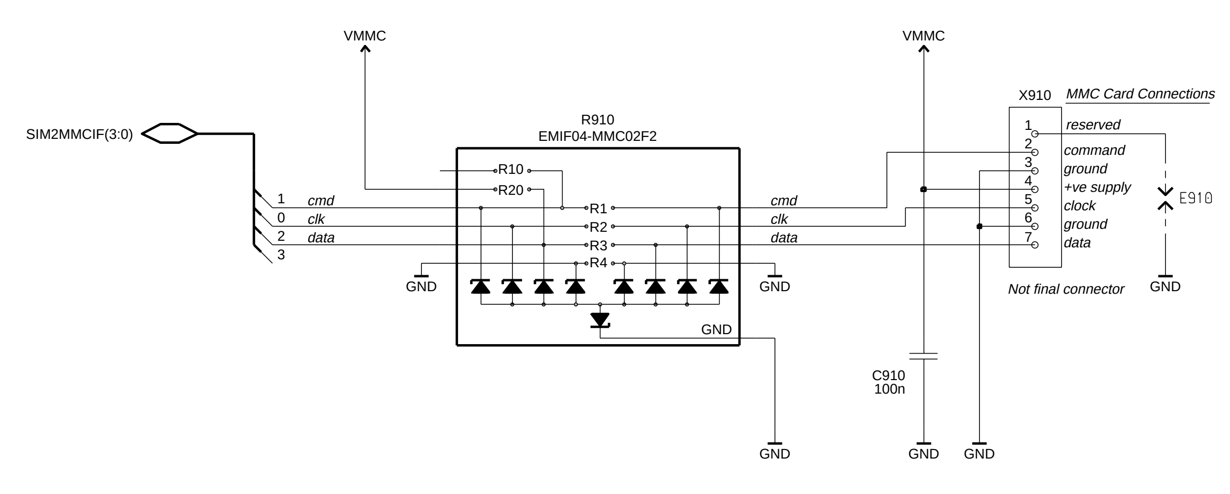


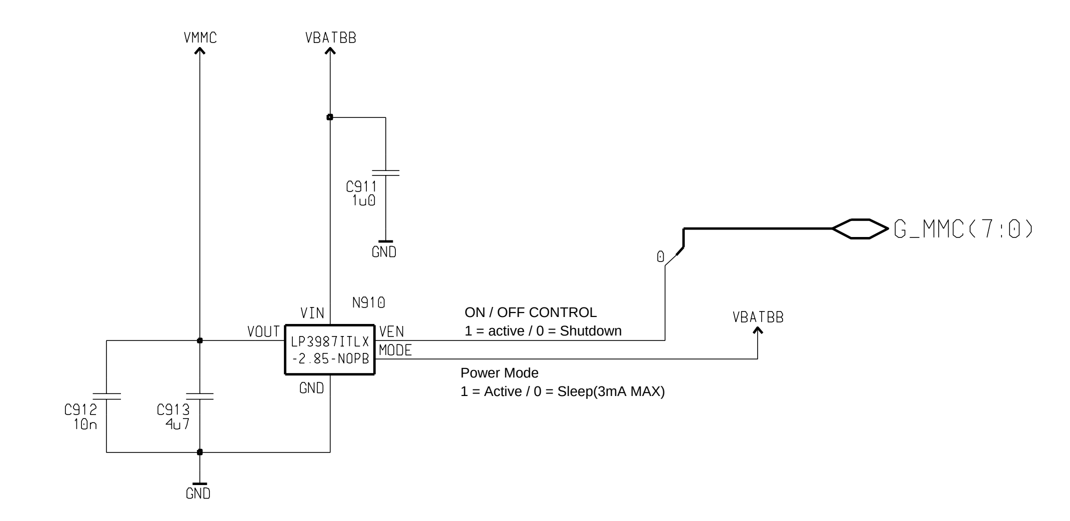

Resistors R451 and R454 are added into schematics to make it possible to replace NAND FLASH by NOR FLASH if necessary. Later when NAND FLASH will achieve more mature state, these resistors will be removed.

In case of NAND FLASH, memory pin A7 and B7 need to be n.c. on PWB. B5 need to be connected to GENIO(13) only.


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**MMC Interface, 1maa_02, v. 2.1 ed. 80**




*NOTE: The MMC specification imposes the following impedance limits*

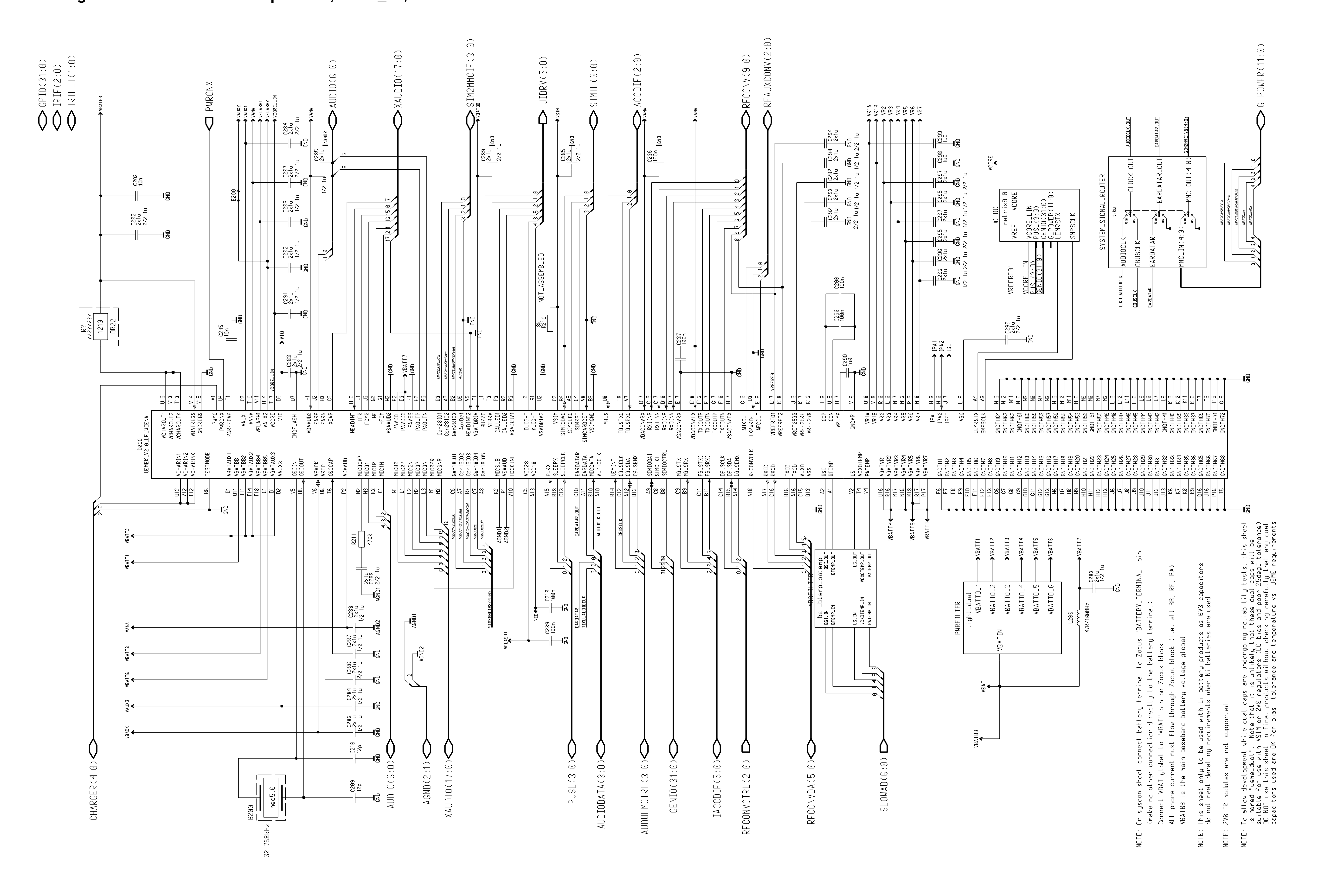
*Command pullup resistance 4.7k to 100k*
*Data pullup resistance 50k to 100k*

*EMIF02-MMC R10=13k (therefore only suitable for command line)*
*EMIF02-MMC R20=56k (therefore only suitable for data line)*
*EMIF02-MMC R1/R2/R3/R4=47R*

*UEME includes a 13k pullup between GEN28IO2 (command) and VAUX1*
*so EMIF02-MMC R10 is left unconnected. UEME has no pullup on GEN28IO3 (MMC data)*

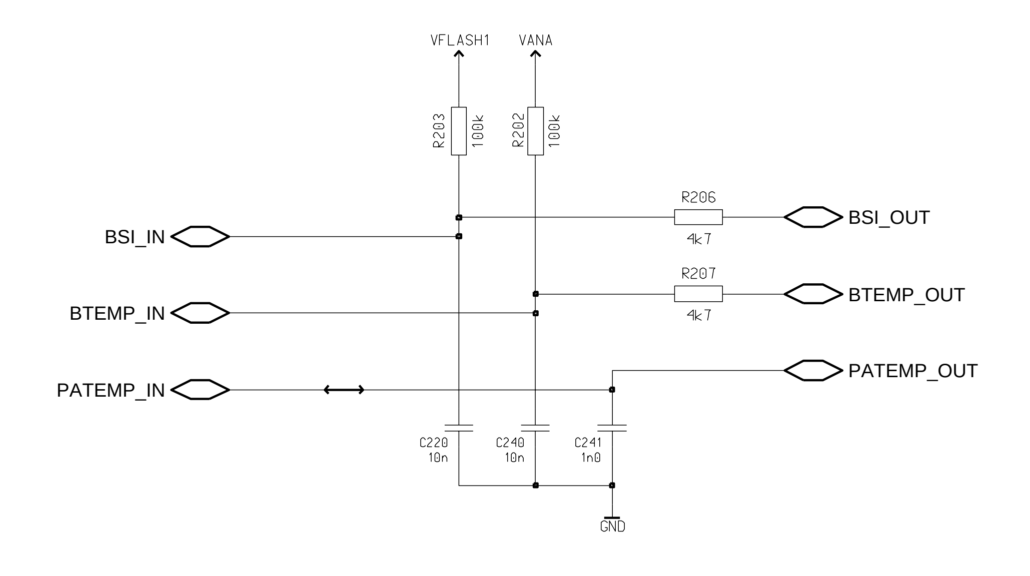
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**UEME Power Management - Dual 1 uF 6V3 Capacitors, 1maa_02, v. 1.0 ed. 321**




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**UEME ADC Filter Block - BSI, BTEMP and Active PATEMP, 1maa_02, v. 1.0**



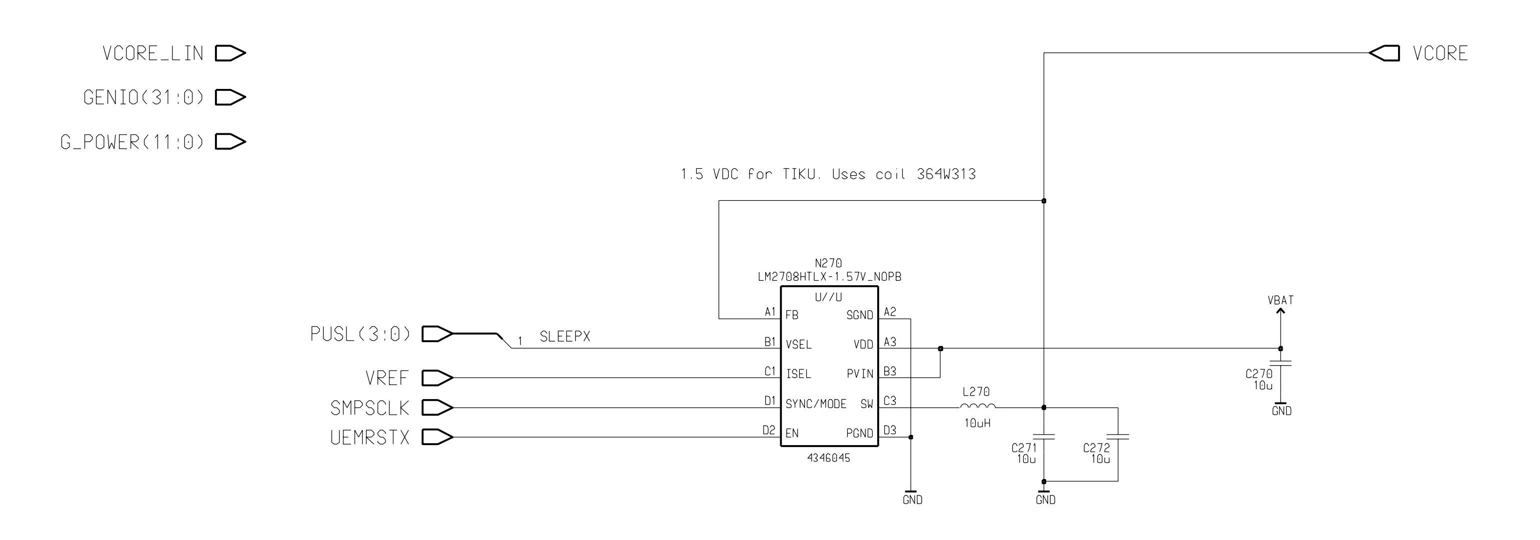




NOTE: Helga RF drives PATEMP directly
so PATEMP does not need a pullup


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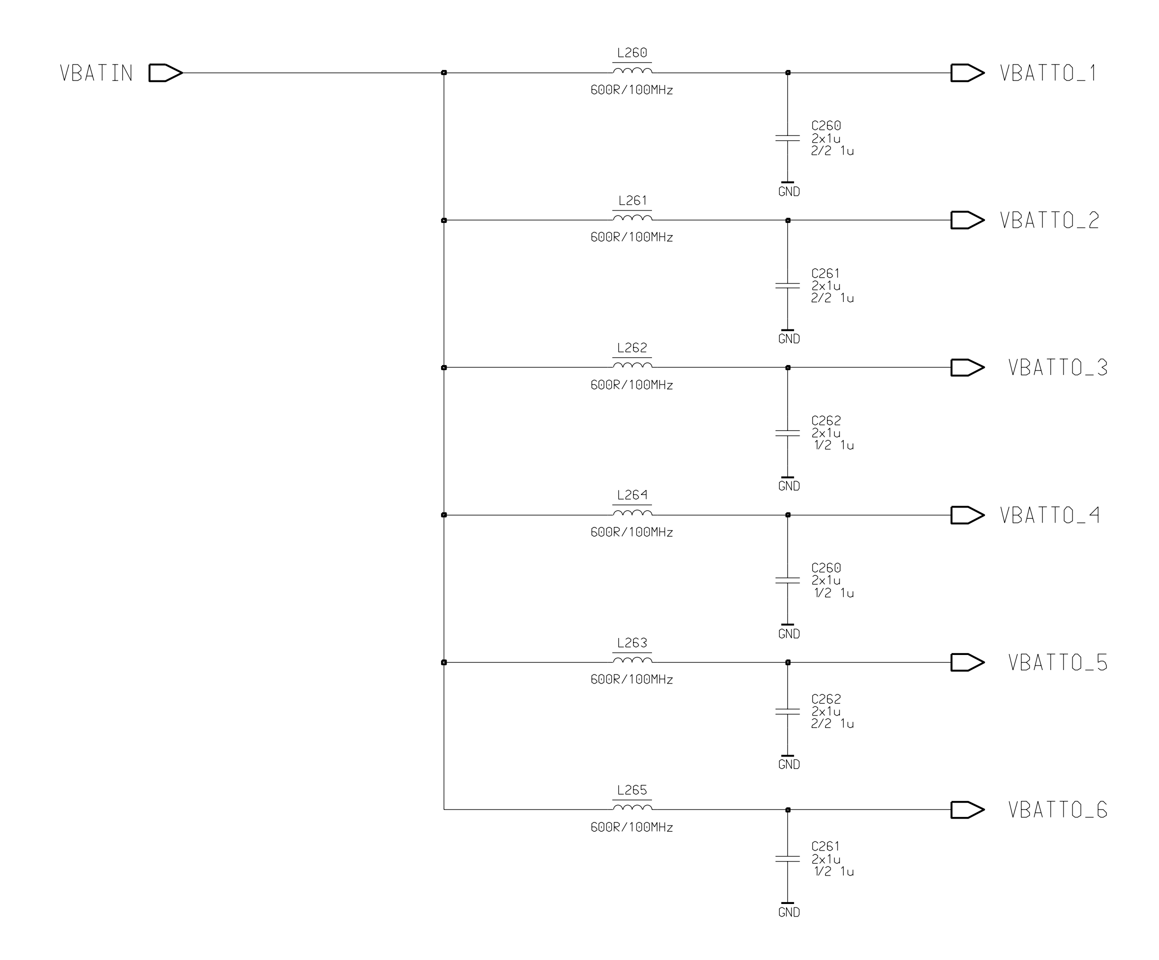
**DC_DC for TIKU and VIO, 1maa_02, v. 1.7 ed. 79**





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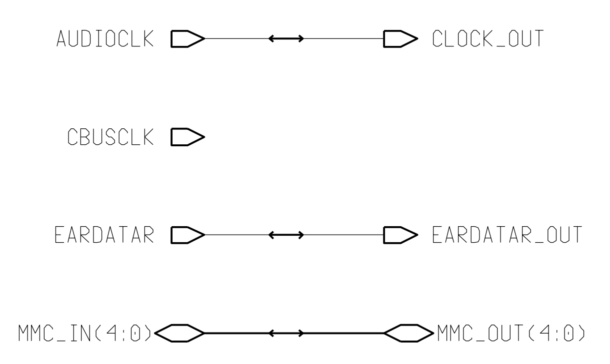
## Light Filtering for Projects using 1uF Caps, 1maa_02, v. 1.0




NOTE: This sheet uses dual 1uF capacitors. Check that full approval has been granted for these parts before use, or use this sheet at risk

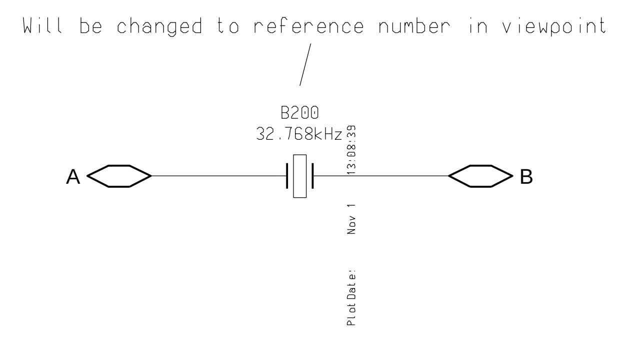

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**Tiku Systems, 1maa_02, v. 1.0**




E916

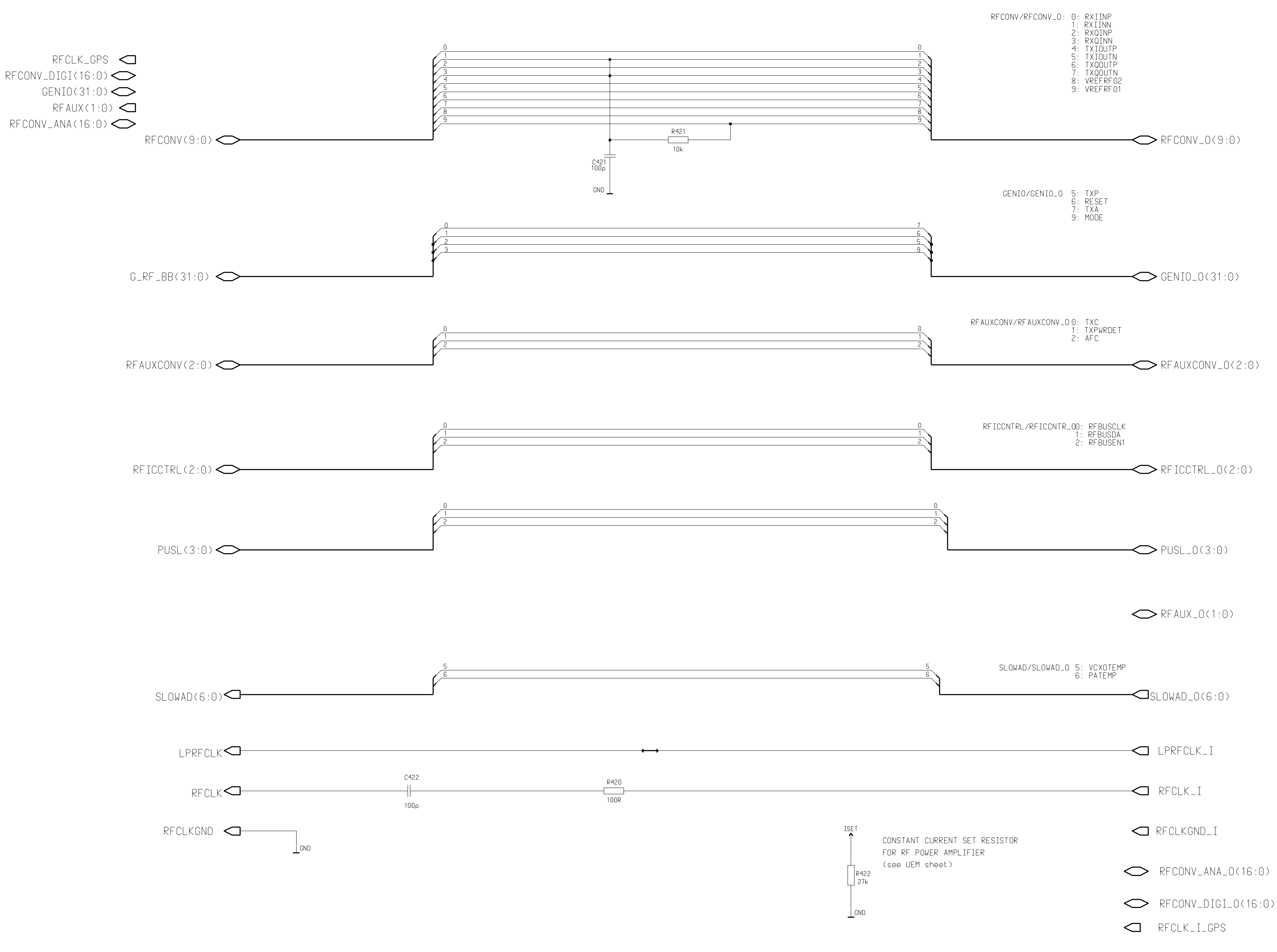
**32KHz Crystal- Micro Crystal CC4VT2, 1maa_02, v. 0 ed. 8**




Plot Date: Nov 1 13:08:39

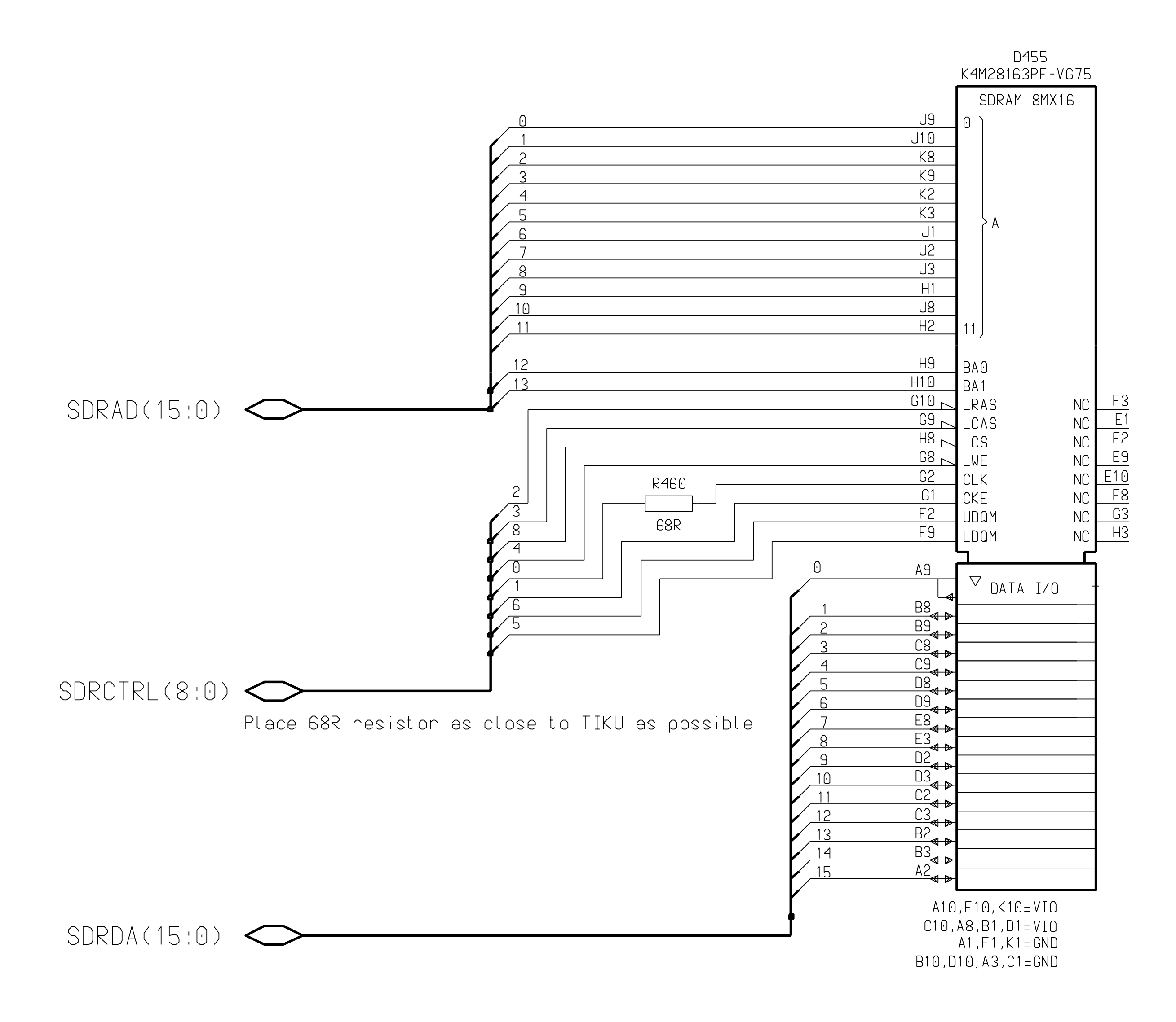
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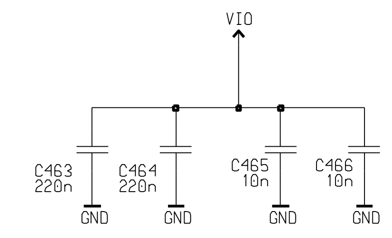
**GSM RF - Baseband Interface, 1maa_02, v. 1.3 ed. 65**




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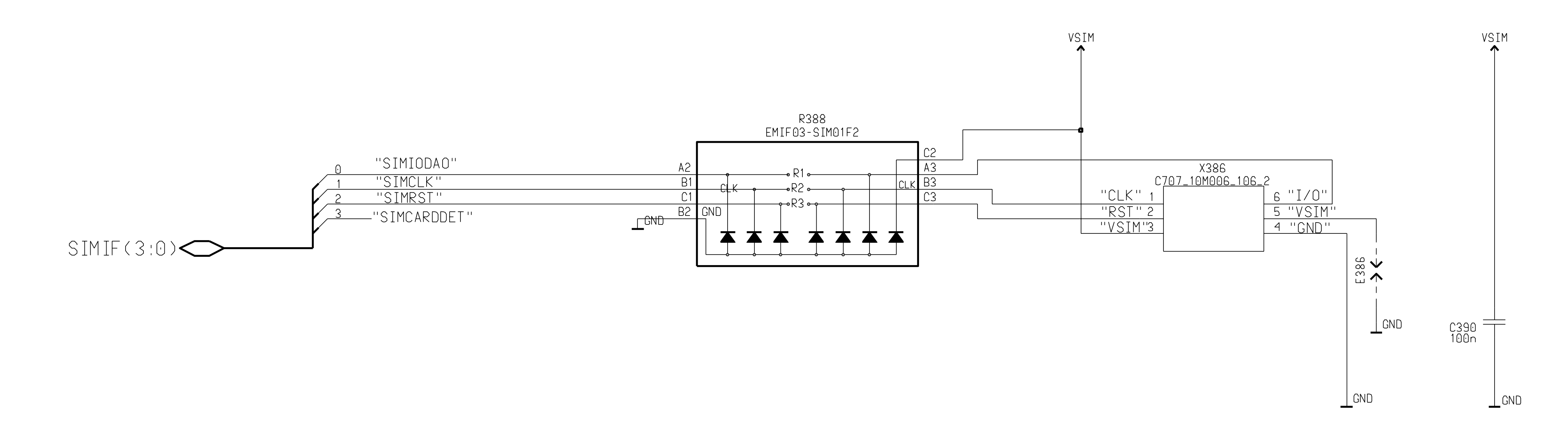
**64Mbit 2V8 SDRAM Memory, 1maa_02, v. 1.0 ed. 68**




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## SIM Reader, 1maa_02, v. 1.3 ed. 67



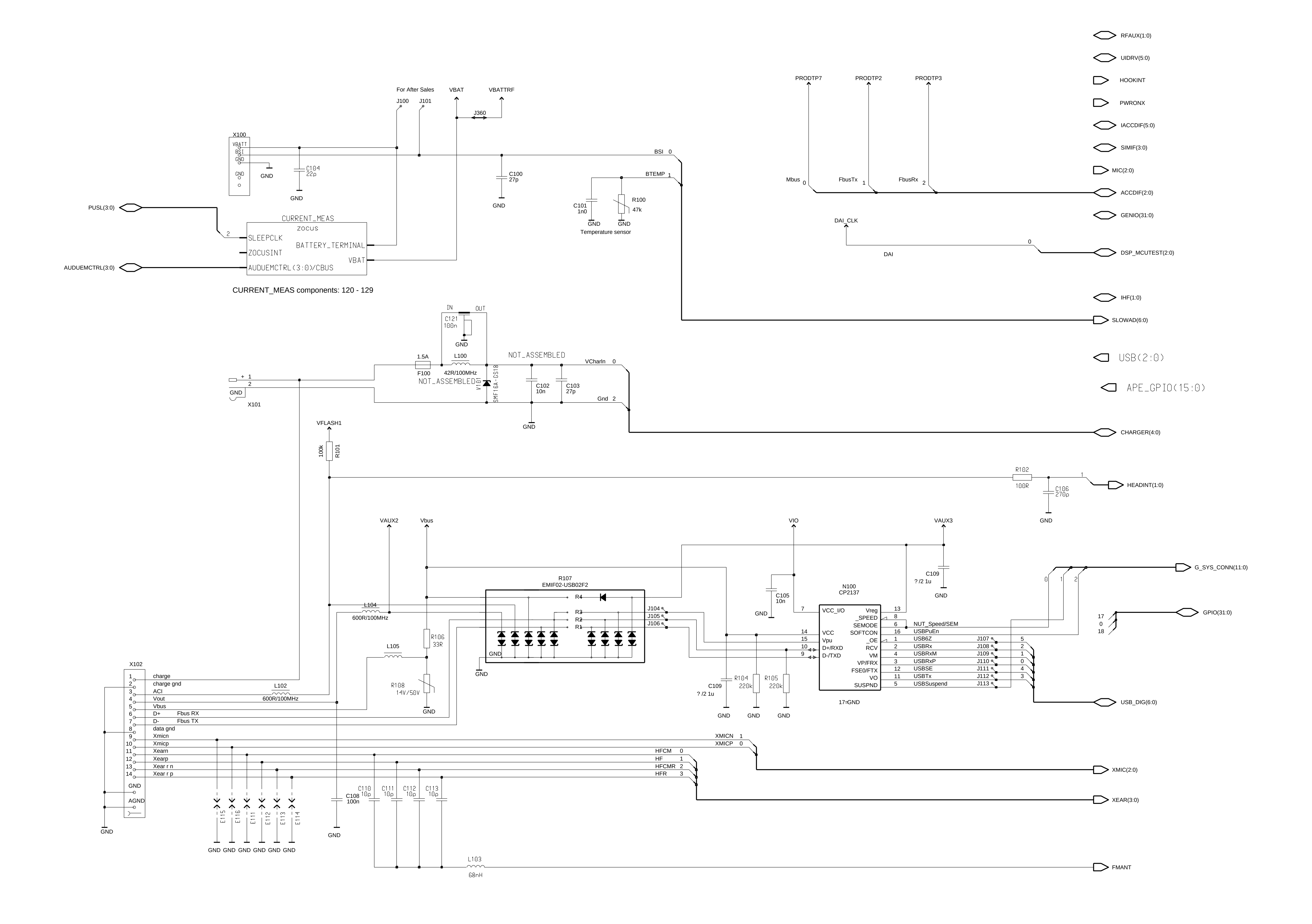

GENIO(31:0)

SIM2MMCIF(3:0)

G_SIM(7:0)

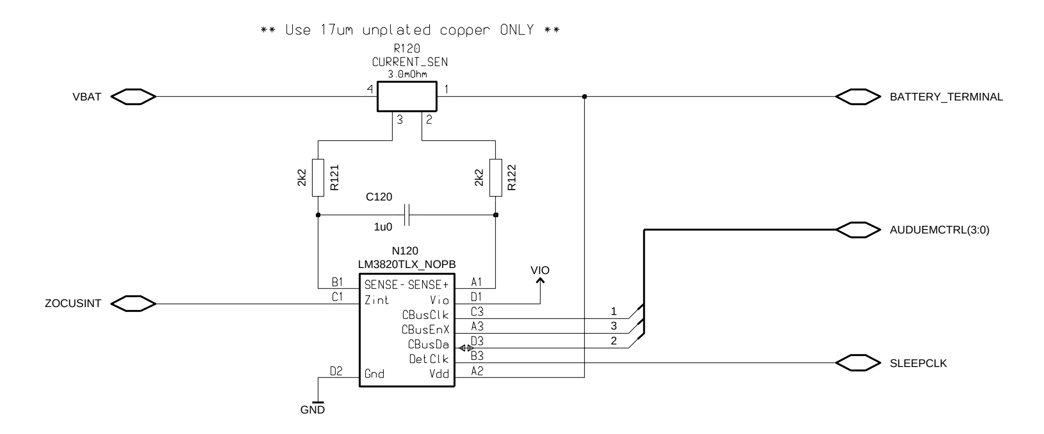

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## System Connector, 1maa_02, v. 0.0 ed. 204

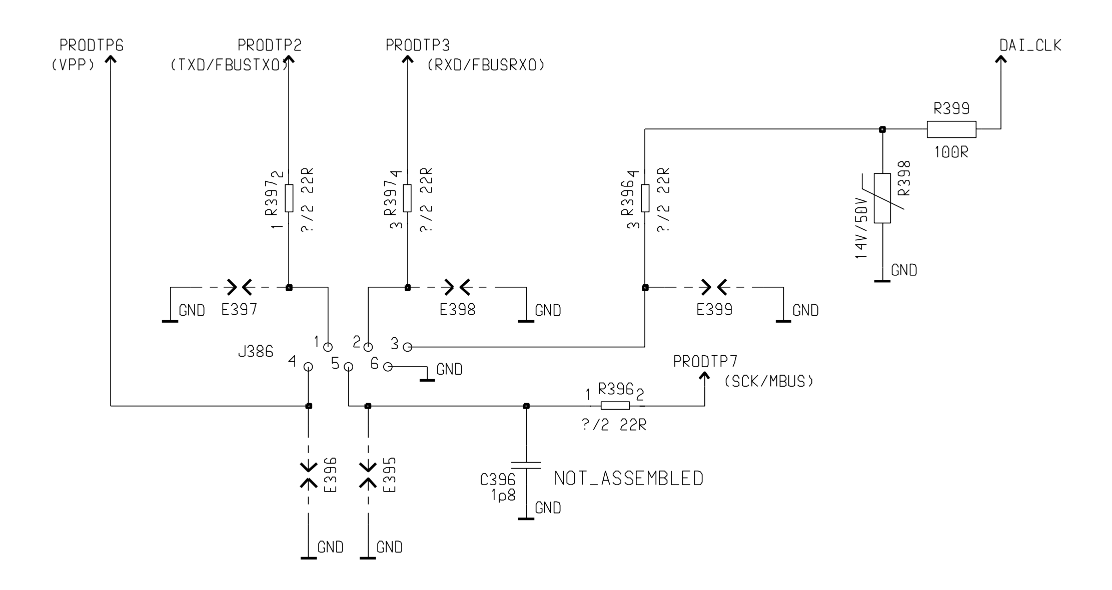



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**Current Measure, 1maa_02, v. 0 ed. 19**



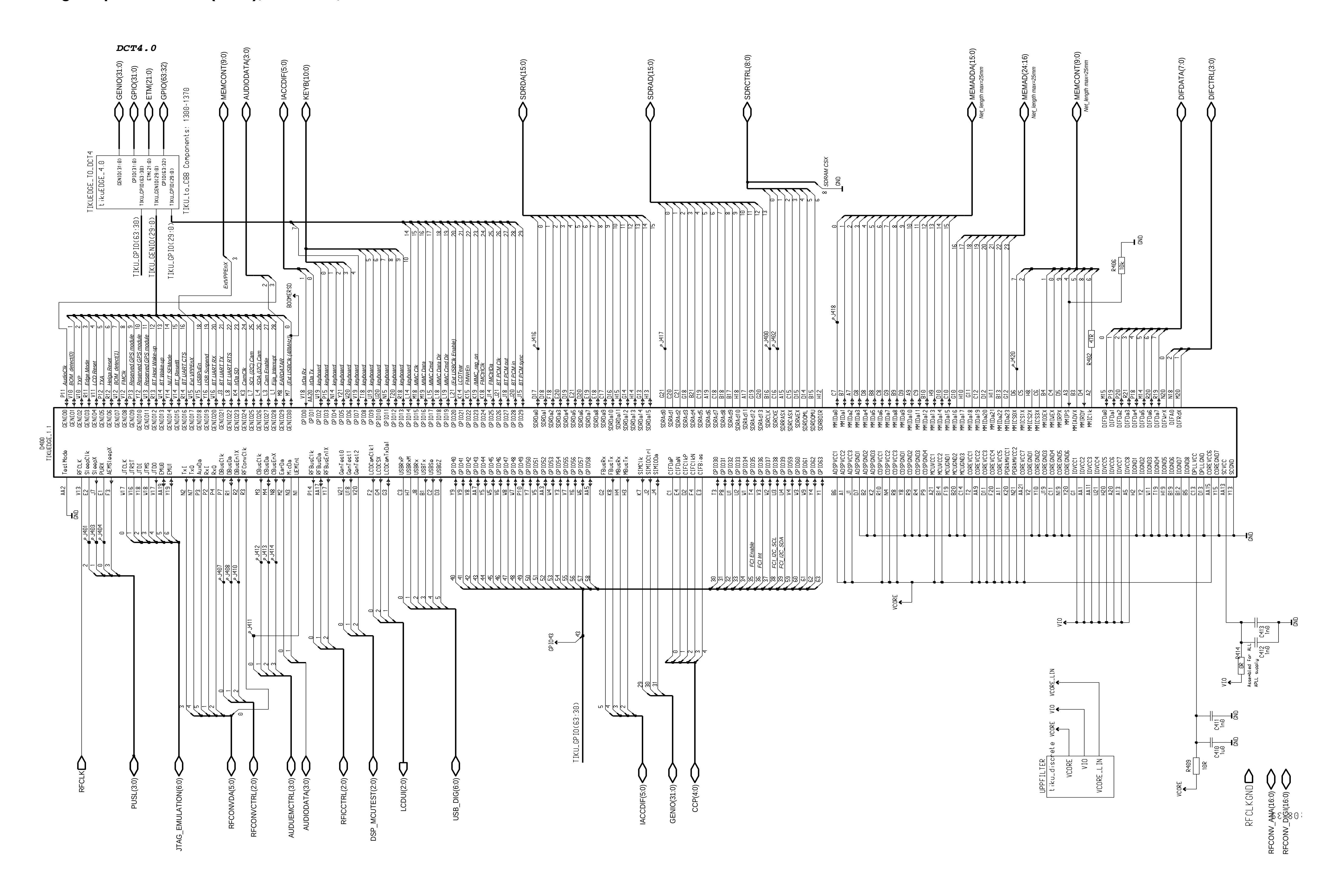

**5 pin Test Pattern, 1maa_02, v. 2.0 ed. 53**





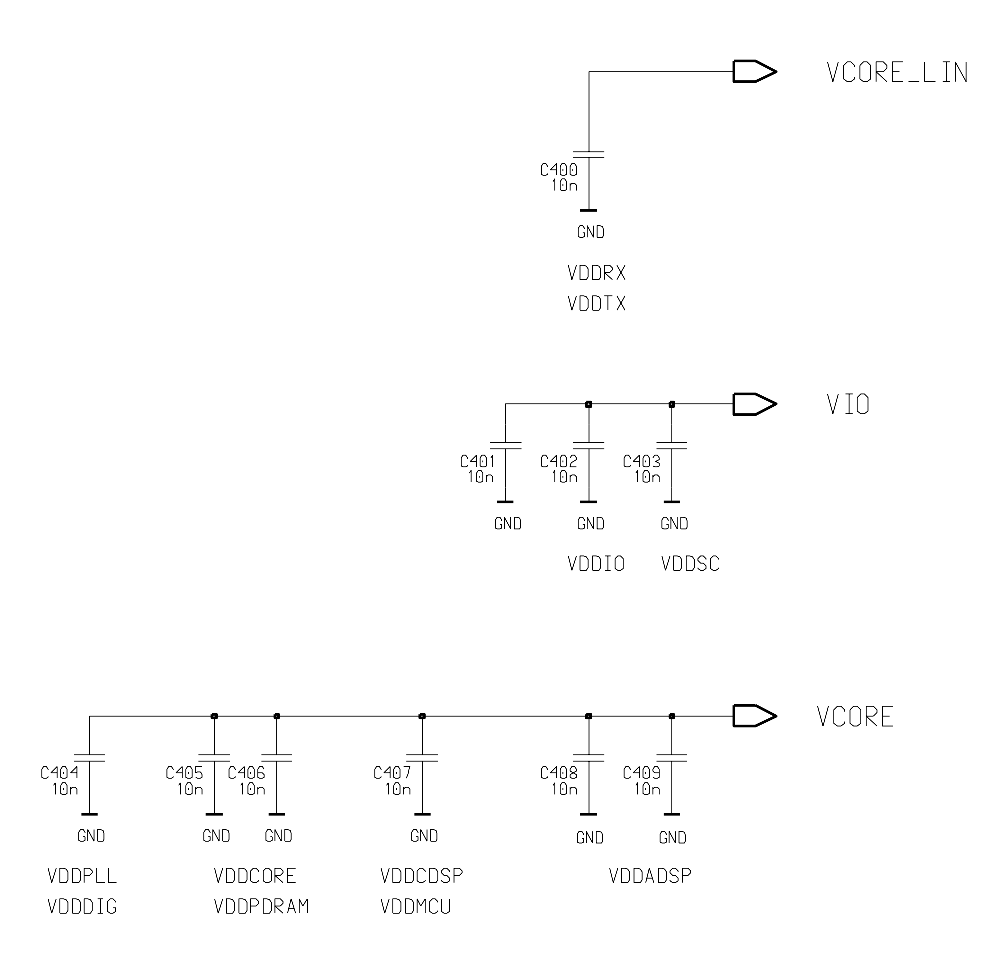
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**TIKUedge Implementation (GSM), 1maa_02, ed. 135**




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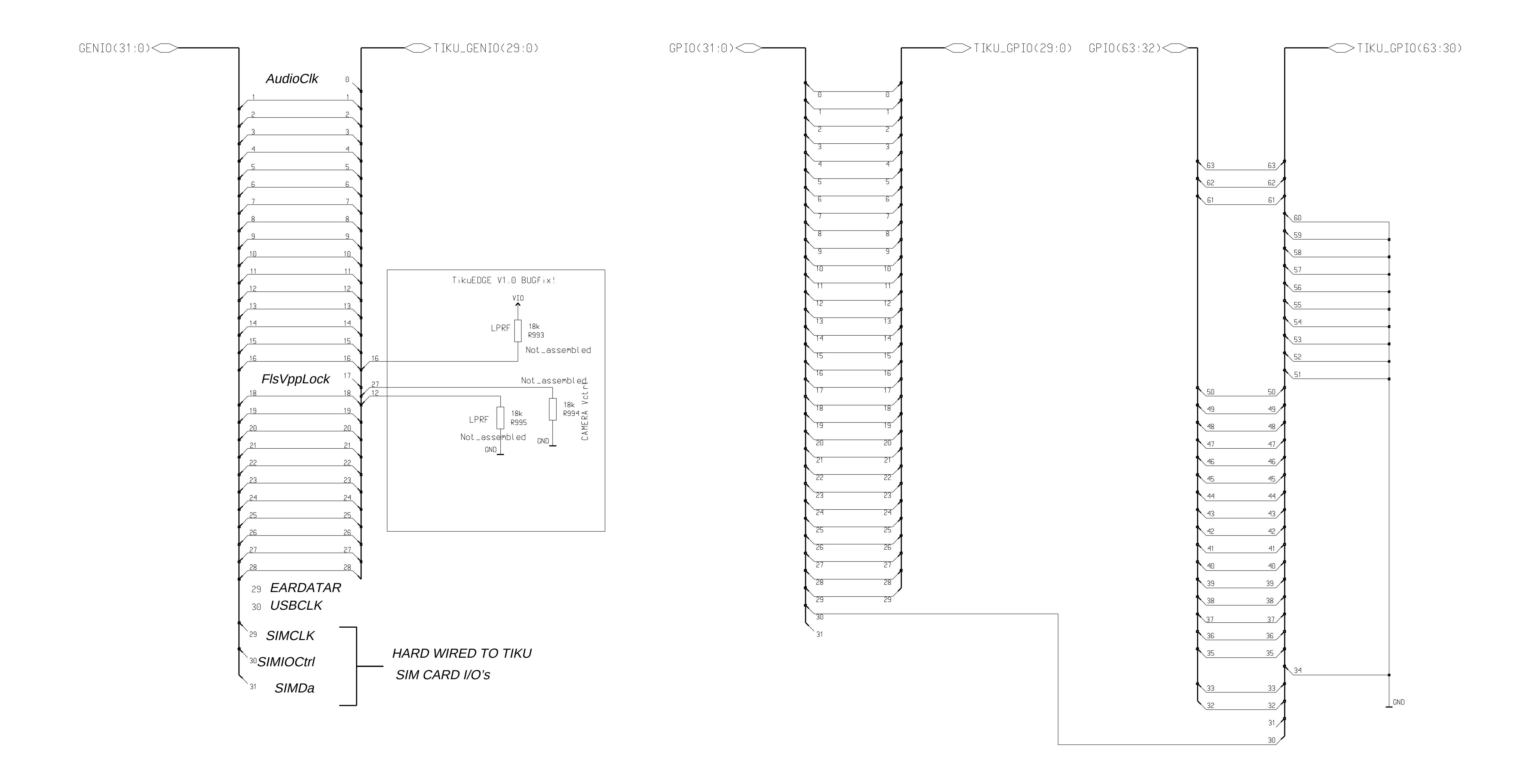
**Discrete Decoupling Capacitors for Tiku, 1maa_02, v. 0 ed. 12**





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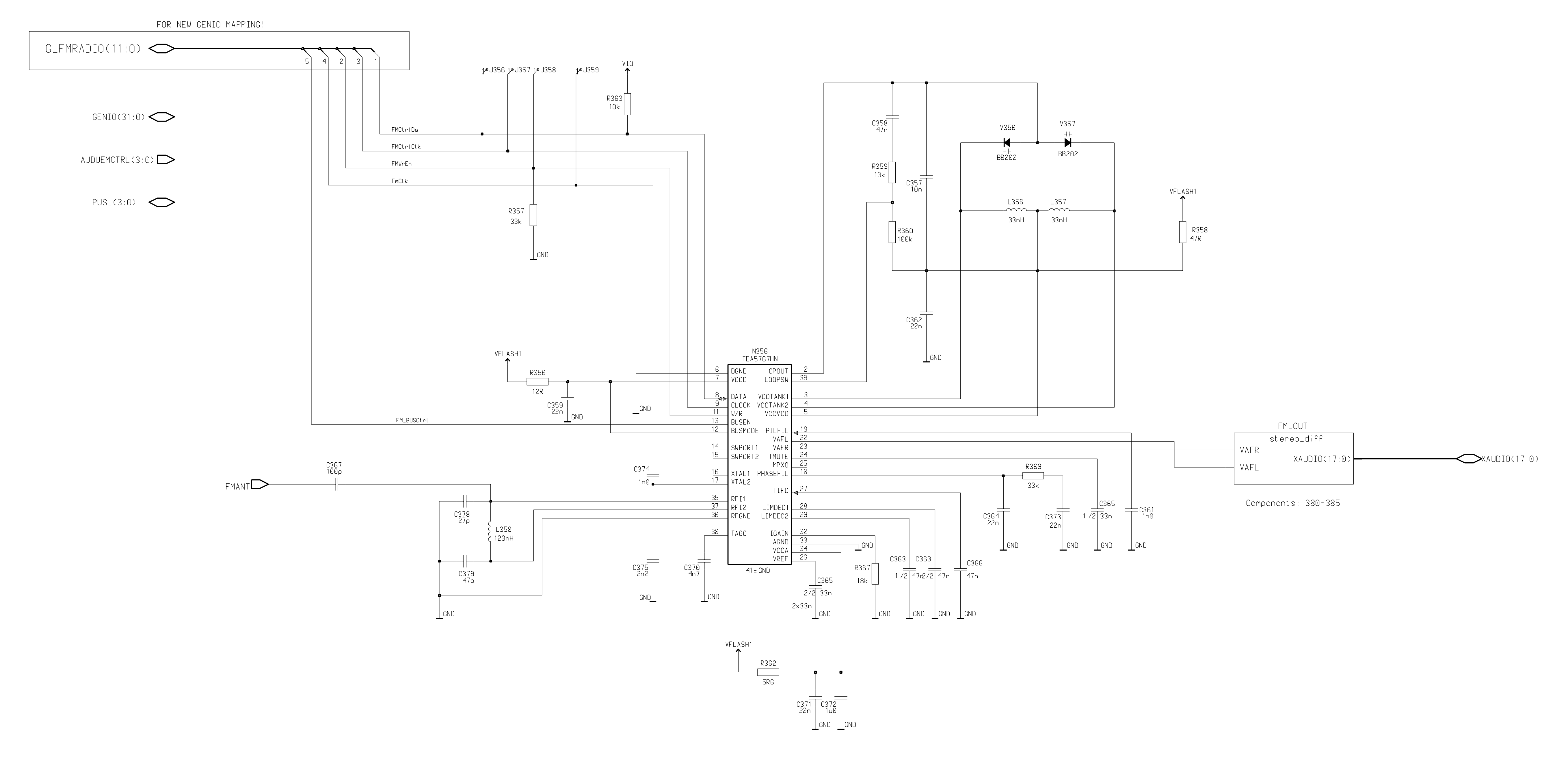
**TikuEDGE Bus Conversion Sheet, 1maa_02, v. 1.2 ed. 104**





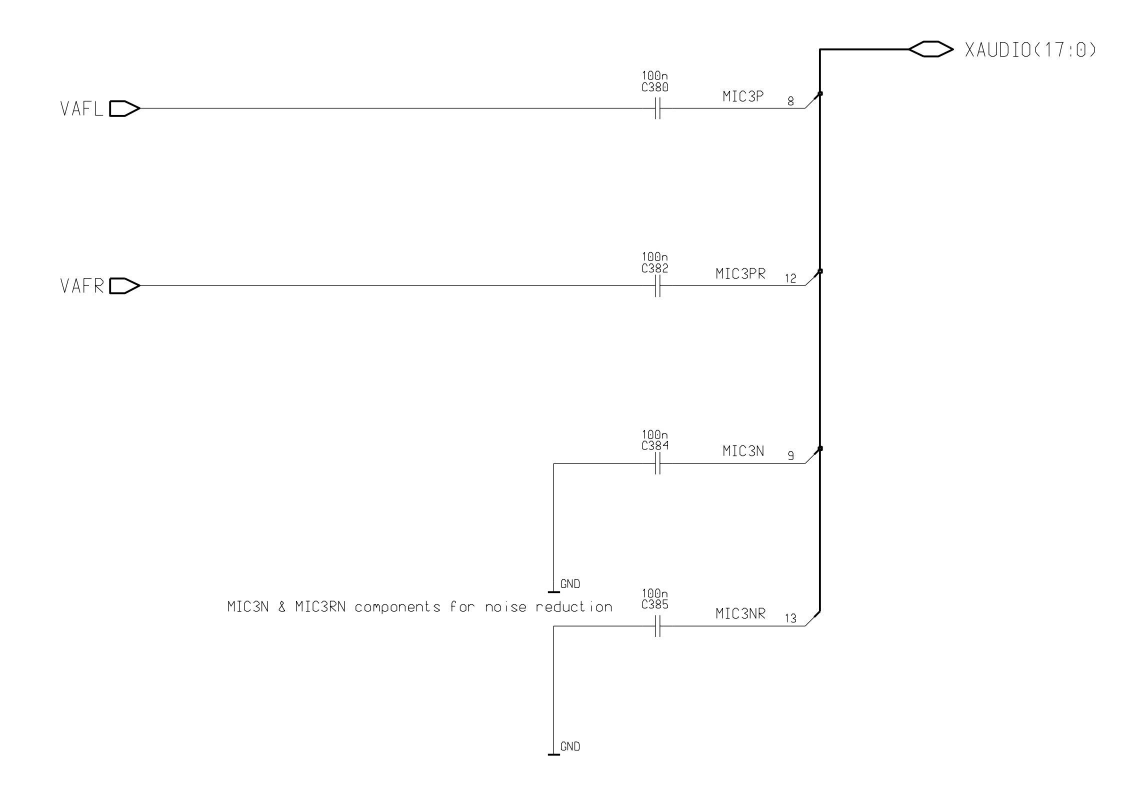
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**FM Radio Unit, 1maa_02, v. 1.3 ed. 199**



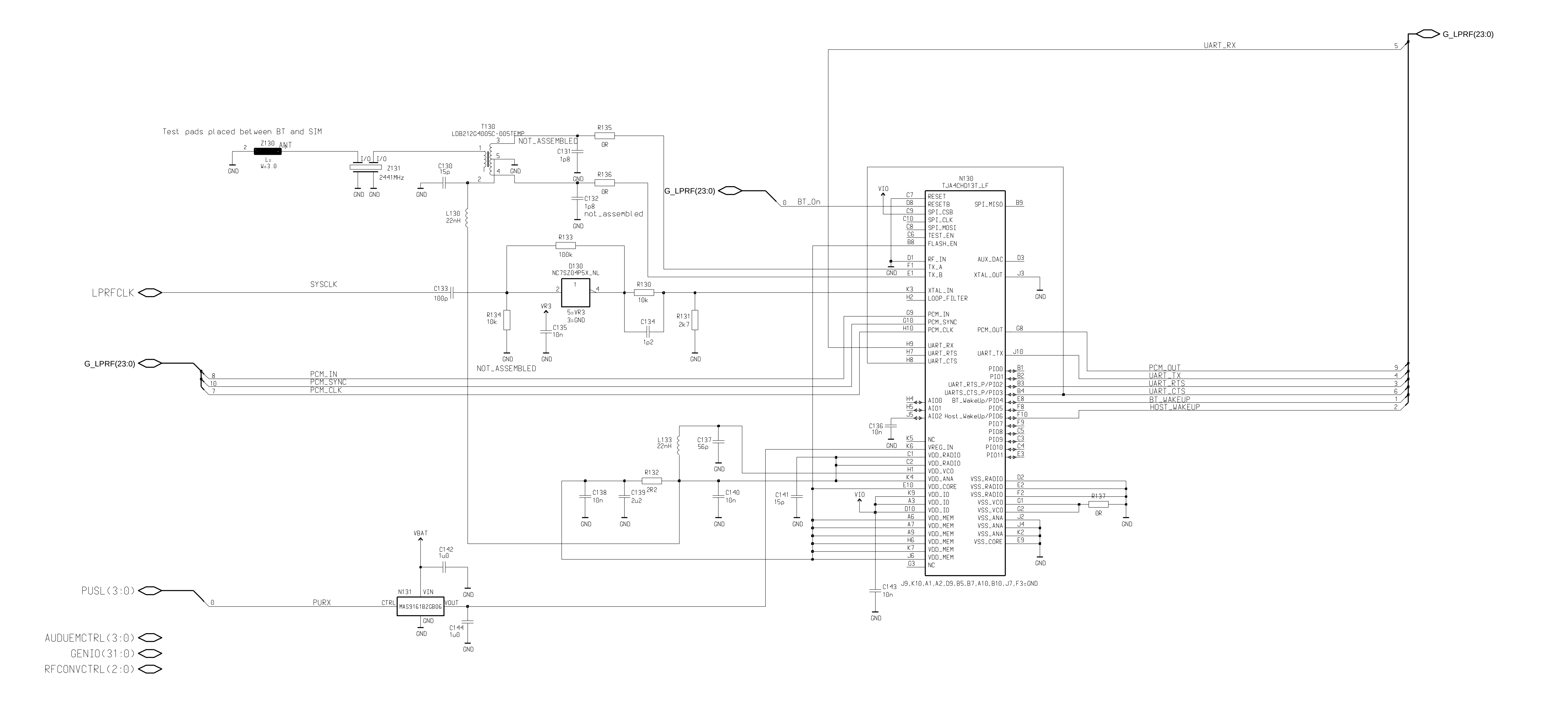

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**Differential Stereo, 1maa_02, v. 0.0 ed. 7**



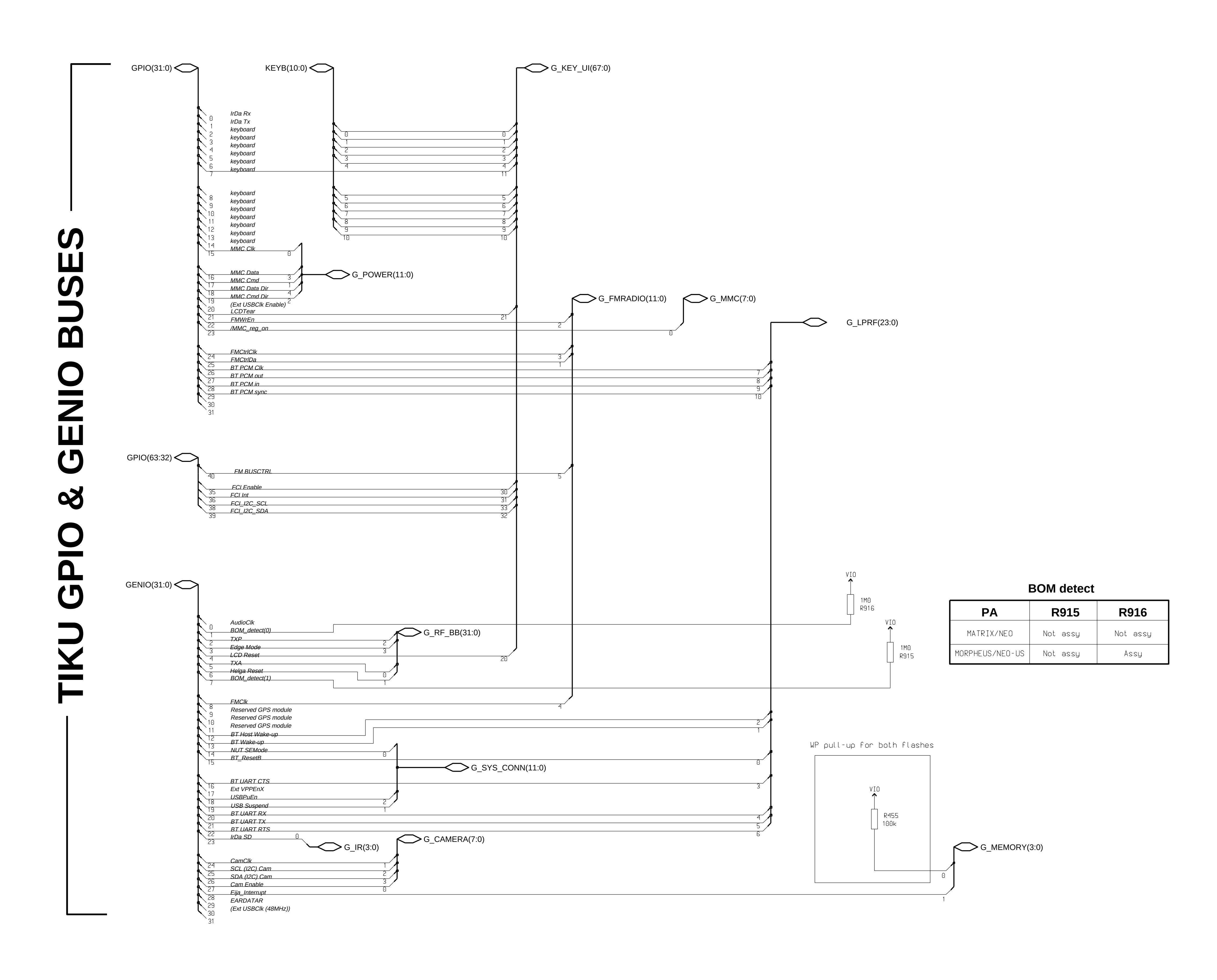

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## Low Power RF Module, 1maa_02, ed. 148



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## GENIO and GPIO Connection Block, 1maa_02, v. 0.0 ed. 42




**BOM detect**

| PA | R915 | R916 |
|---|---|---|
| MATRIX/NEO | Not assy | Not assy |
| MORPHEUS/NEO-US | Not assy | Assy |


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